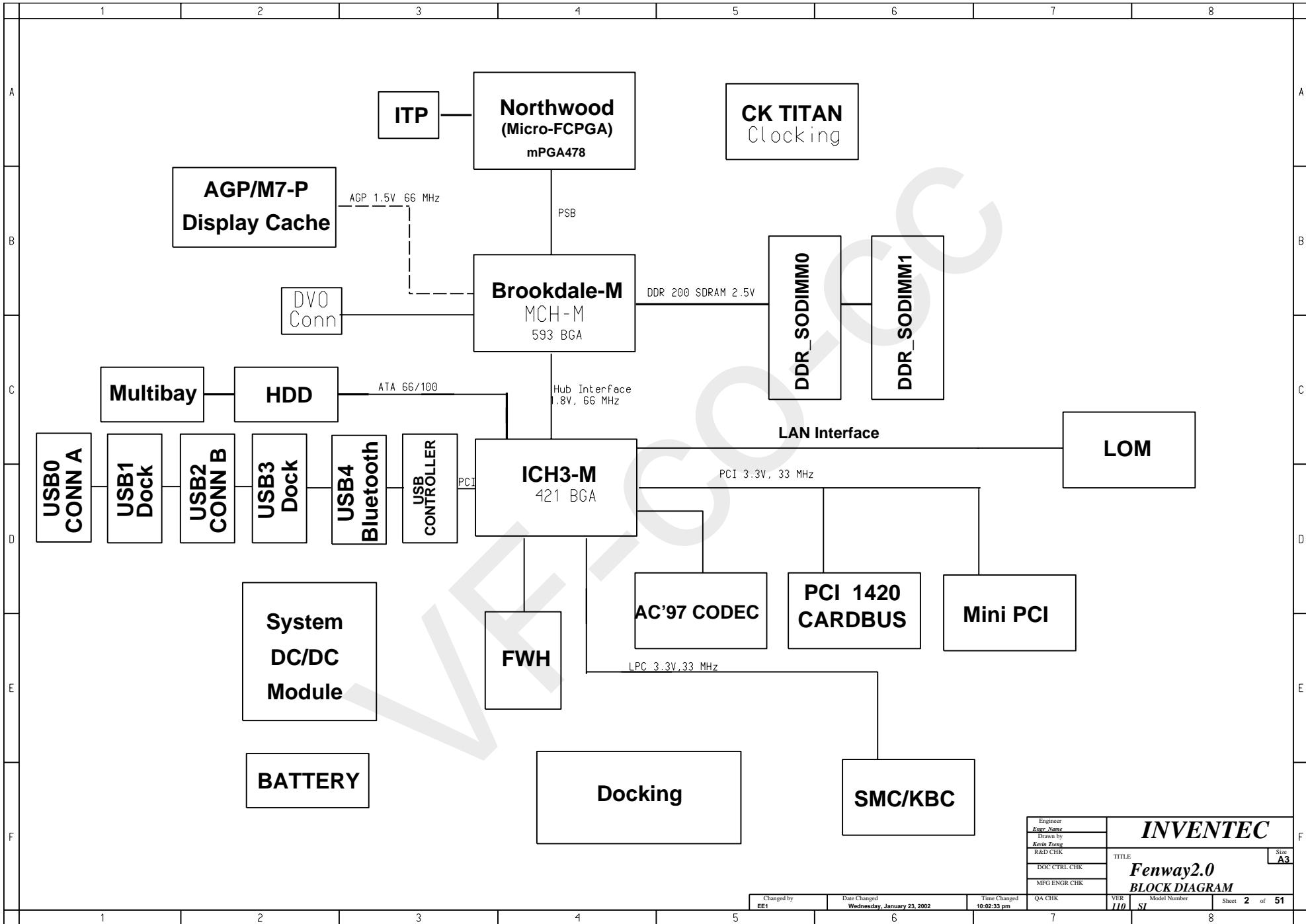


# TABLE OF CONTENTS

Revisions			Approvals			
REV	ECN NO.	Description Of Change	DATE	DFTG.	ENGR.	REL.

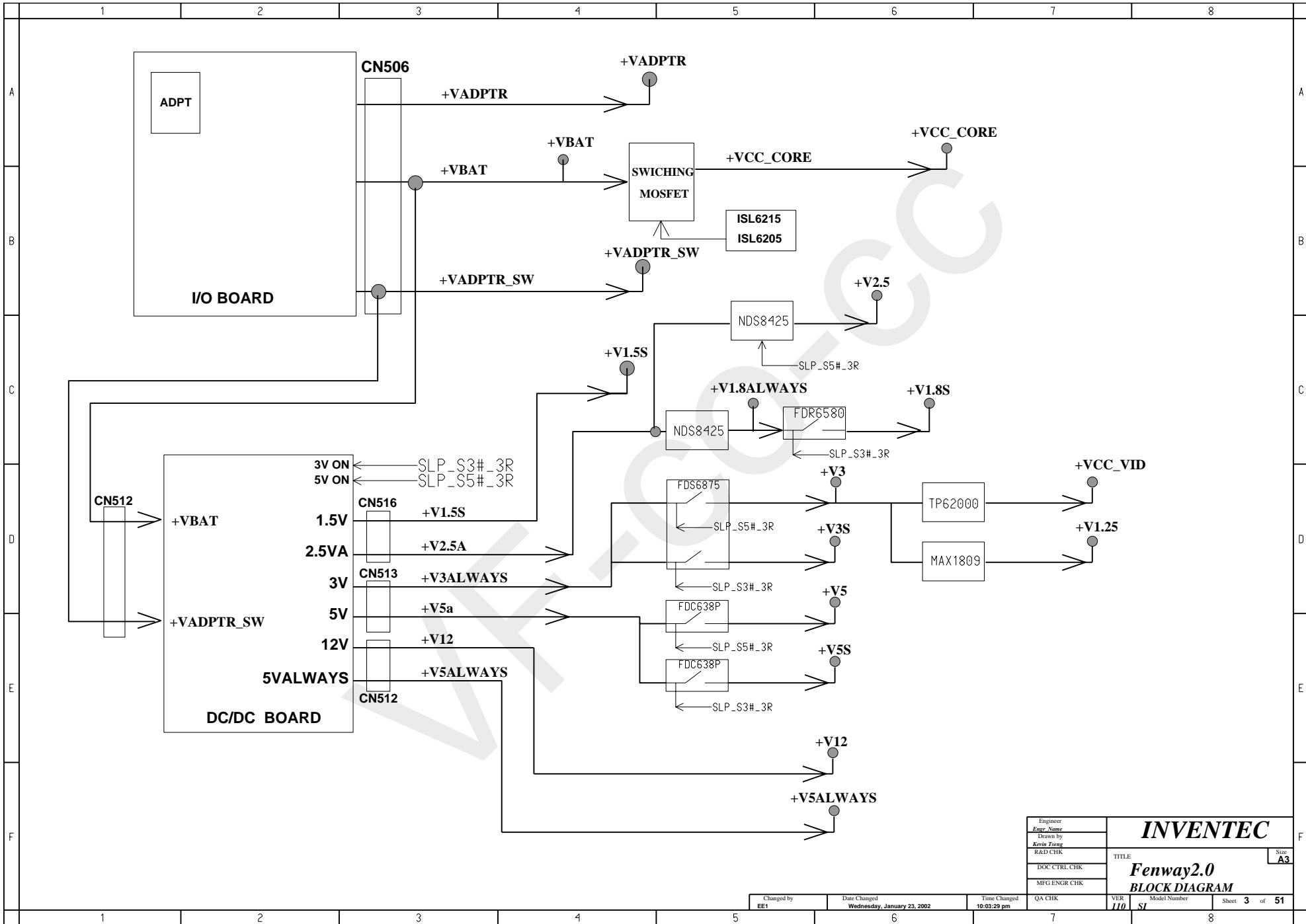
PAGE		PAGE	
4	- CLOCK GENERATOR	32	- DUALBAY
5-7	- NORTHWOOD PROCESSOR	33	- INT KBD./ POINTING DEVICE
8-10	- BROOKDALE	34	- LID SWITCH/LEDS
11-14	- DDR	35	- DOCKING CONNECTOR
15,16	- ATI_M7_P	36	- MINI PCI
17	- VIDEO RAM	37	- USB, IR
18	- CRT,LCD,INVERTER CONNECTOR INTERFACE	38	- BLUETOOTH
19,20	- ICH3-M	39	- THERMAL SENSOR & FAN CONTROLLER
21	- KAHUNA	40	- ADAPTER SENSE CIRCUIT & CLAMP CIRCUIT
22	- FWH & EEPROM	41	- PWR MODULE CONN
23	- SERIAL , PARALLEL AND PS2 PORTS	42	- DECOUPLING CAPS
24	- CARDBUS CONTROLLER	43	- DRILL HOLE
25	- PC CARD SLOT	44	- HIGH SPEED
26	- AC97 CODEC	45	- PULL UPS
27	- EQ & MIC JACK	46	- LAN INTERFACE
28	- AUDIO AMP & HP JACK	47-49	- POWER
29	- HDD AND MB1 PWR AND SWITCH	50	- USB2.0
30	- HDD CONNECTOR	51	- IO BOARD
31	- MULTIBAY		

Engineer <i>Engr Name</i>	<b>INVENTEC</b>
Drawn by <i>Kevin Tseng</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	
TITLE	<b>Fenway2.0</b>
<b>TABLE OF CONTENTS</b>	
VER	Size <b>A3</b>
110	SI



Engineer		<b>INVENTEC</b>
Engr Name		
Drawn by	Kevin Tseng	
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
TITLE		<b>Fenway2.0</b>
BLOCK DIAGRAM		Size <b>A3</b>

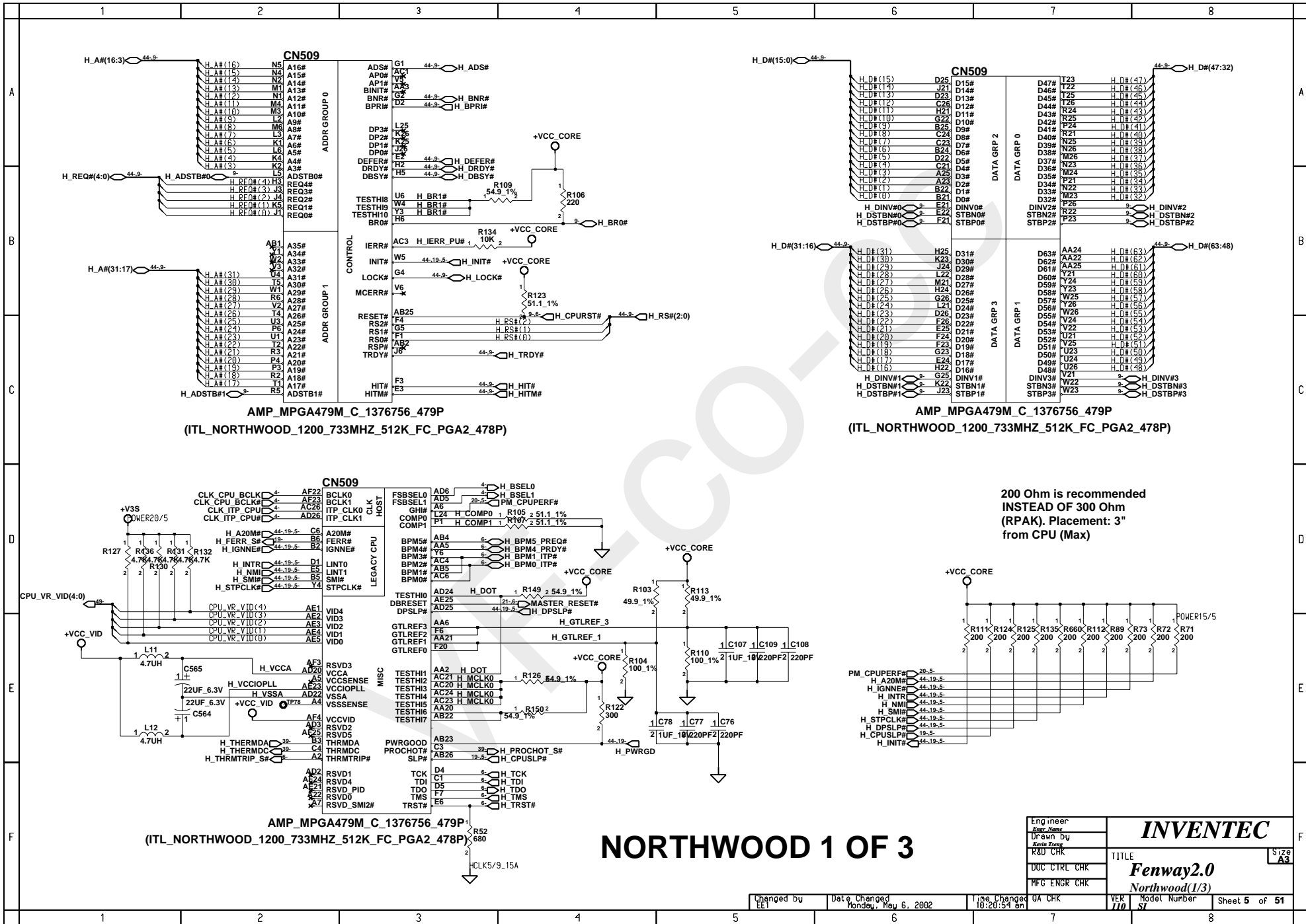
Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of	51
EE1	Wednesday, January 23, 2002	10:02:33 pm		110	SI	2		



Engineer		<b>INVENTEC</b> TITLE: <b>Fenway2.0</b> <b>BLOCK DIAGRAM</b>	Size	A3
Engr Name				
Drawn by	Kevin Tseng			
R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK				

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet	of	51
EE1	Wednesday, January 23, 2002	10:03:29 pm		110	SI	3		





AMP\_MPGA479M\_C\_1376756\_479P

(ITL\_NORTHWOOD\_1200\_733MHZ\_512K\_FC\_PGA2\_478P)

AMP\_MPGA479M\_C\_1376756\_479P

(ITL\_NORTHWOOD\_1200\_733MHZ\_512K\_FC\_PGA2\_478P)

200 Ohm is recommended  
INSTEAD OF 300 Ohm  
(RPAK). Placement: 3"  
from CPU (Max)

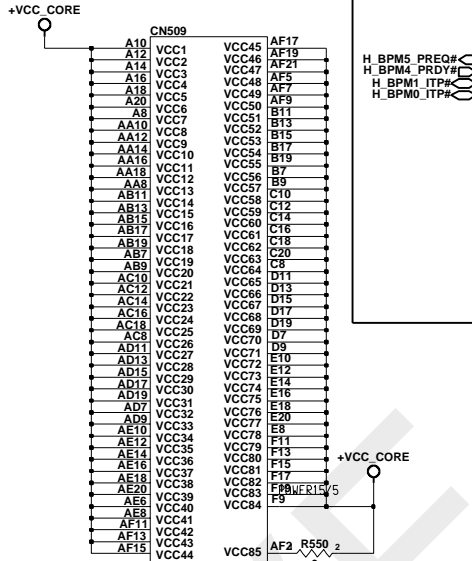
# NORTHWOOD 1 OF 3

Eng Ineer		<b>INVENTEC</b>
Drawn By		
Revise		
R3D CHK		TITLE
DOC CTRL CHK		<b>Fenway2.0</b>
MFG ENGR CHK		Northwood(1/3)

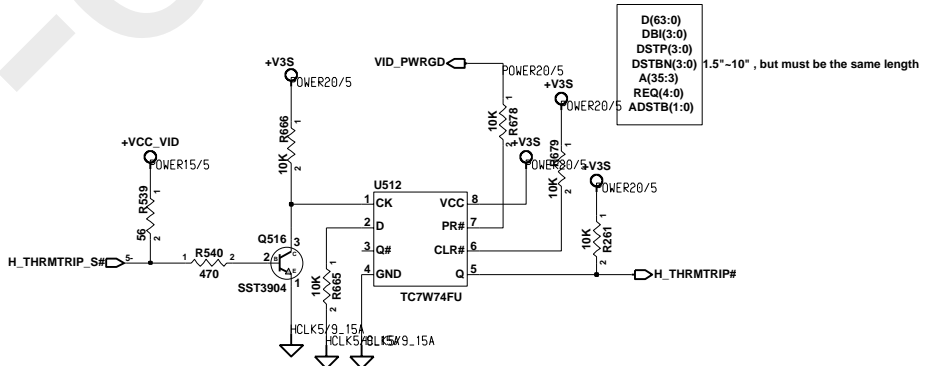
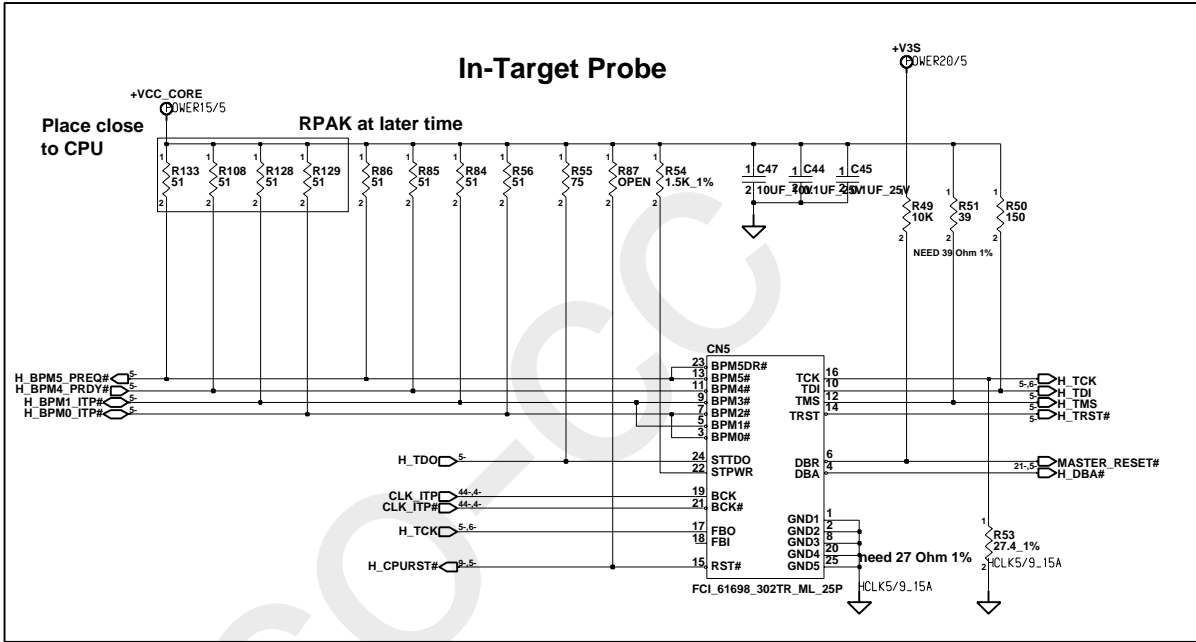
Changed by	Date Changed	Time Changed	UA CHK	VER	Model Number	Sheet 5 of 51
EE1	Monday, Nov 6, 2002	10:20:54 am		110	SI	

**CN509**

H1	VSS1	VSS92	C19
H4	VSS2	VSS93	C2
H26	VSS3	VSS94	C25
A11	VSS4	VSS95	C5
A13	VSS5	VSS96	C7
A15	VSS6	VSS97	C9
A17	VSS7	VSS98	D10
A19	VSS8	VSS99	D12
A21	VSS9	VSS100	D14
A24	VSS10	VSS101	D16
A26	VSS11	VSS102	D18
A3	VSS12	VSS103	D20
A9	VSS13	VSS104	D22
AA1	VSS14	VSS105	D24
AA11	VSS15	VSS106	D3
AA13	VSS16	VSS107	D6
AA15	VSS17	VSS108	D8
AA17	VSS18	VSS109	E1
AA19	VSS19	VSS110	E11
AA23	VSS20	VSS111	E13
AA25	VSS21	VSS112	E15
AA27	VSS22	VSS113	E17
AA4	VSS23	VSS114	E19
AA7	VSS24	VSS115	E23
AA9	VSS25	VSS116	E26
AB10	VSS26	VSS117	E4
AB11	VSS27	VSS118	E7
AB14	VSS28	VSS119	E9
AB16	VSS29	VSS120	E10
AB18	VSS30	VSS121	F12
AB21	VSS31	VSS122	F14
AB24	VSS32	VSS123	F16
AB3	VSS33	VSS124	F18
AB6	VSS34	VSS125	F2
AB8	VSS35	VSS126	F22
AC11	VSS36	VSS127	F25
AC13	VSS37	VSS128	F25
AC15	VSS38	VSS129	F8
AC17	VSS39	VSS130	G21
AC19	VSS40	VSS131	G24
AC22	VSS41	VSS132	G24
AC25	VSS42	VSS133	G6
AC9	VSS43	VSS134	G6
AC9	VSS44	VSS135	J2
AC9	VSS45	VSS136	J25
AC9	VSS46	VSS137	J5
AD1	VSS47	VSS138	K21
AD10	VSS48	VSS139	K3
AD12	VSS49	VSS140	K6
AD14	VSS50	VSS141	K6
AD16	VSS51	VSS142	LT
AD18	VSS52	VSS143	L23
AD19	VSS53	VSS144	L26
AD21	VSS54	VSS145	L26
AD23	VSS55	VSS146	L4
AD4	VSS56	VSS147	M2
AD6	VSS57	VSS148	M25
AE13	VSS58	VSS149	M5
AE15	VSS59	VSS150	N21
AE17	VSS60	VSS151	N24
AE19	VSS61	VSS152	N3
AE22	VSS62	VSS153	N6
AE24	VSS63	VSS154	N24
AE7	VSS64	VSS155	P2
AE9	VSS65	VSS156	P25
AE9	VSS66	VSS157	P5
AF1	VSS67	VSS158	RT
AF10	VSS68	VSS159	R23
AF12	VSS69	VSS160	R26
AF14	VSS70	VSS161	R4
AF16	VSS71	VSS162	T1
AF18	VSS72	VSS163	T24
AF20	VSS73	VSS164	T3
AF26	VSS74	VSS165	T7
AF5	VSS75	VSS166	T6
AF5	VSS76	VSS167	U22
B10	VSS77	VSS168	U25
B12	VSS78	VSS169	U5
B14	VSS79	VSS170	U5
B16	VSS80	VSS171	V23
B18	VSS81	VSS172	V26
B20	VSS82	VSS173	V4
B22	VSS83	VSS174	W21
B24	VSS84	VSS175	W24
B4	VSS85	VSS176	W3
B8	VSS86	VSS177	W6
C11	VSS87	VSS178	Y2
C13	VSS88	VSS179	Y22
C15	VSS89	VSS180	Y25
C17	VSS90	VSS181	Y5
C17	VSS91	VSS182	Y5



**AMP\_MPGA479M\_C\_1376756\_479P**  
(ITL\_NORTHWOOD\_1200\_733MHZ\_512K\_FC\_PGA2\_478P)

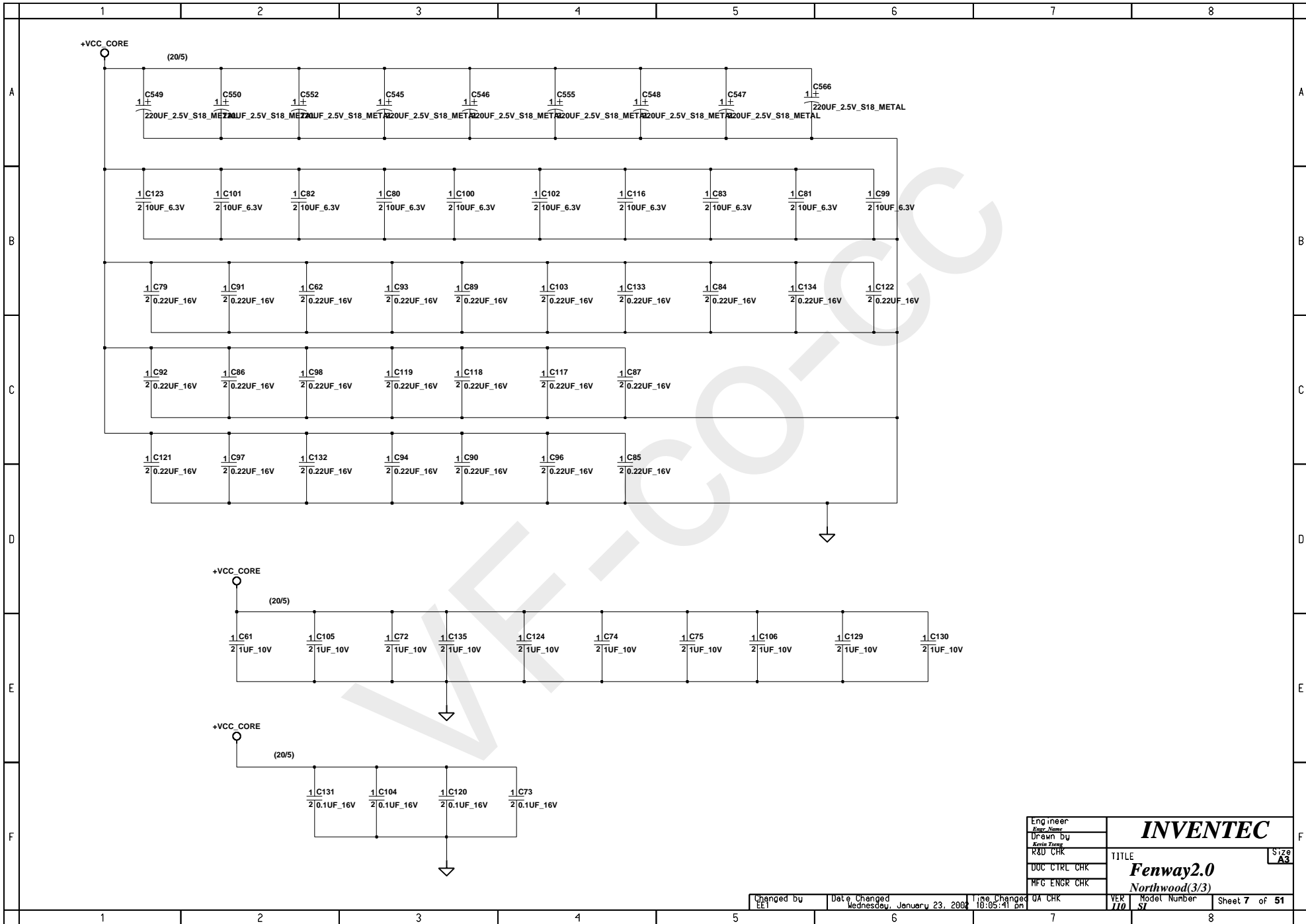


**AMP\_MPGA479M\_C\_1376756\_479P**  
(ITL\_NORTHWOOD\_1200\_733MHZ\_512K\_FC\_PGA2\_478P)

**NORTHWOOD 2 OF 3**

Eng Name		<b>INVENTEC</b>
Drawn by		
Rev'd by		
Rev'd Date		
DOC CTRL		TITLE
ENG ENGR		<b>Fenway2.0</b>
		<b>Northwood(2/3)</b>

Changed by	Date Changed	Time Changed	UA CHK	VER	Model Number	Sheet 6 of 51
EET	Friday, April 19, 2002	10:25:08 am		110	SI	



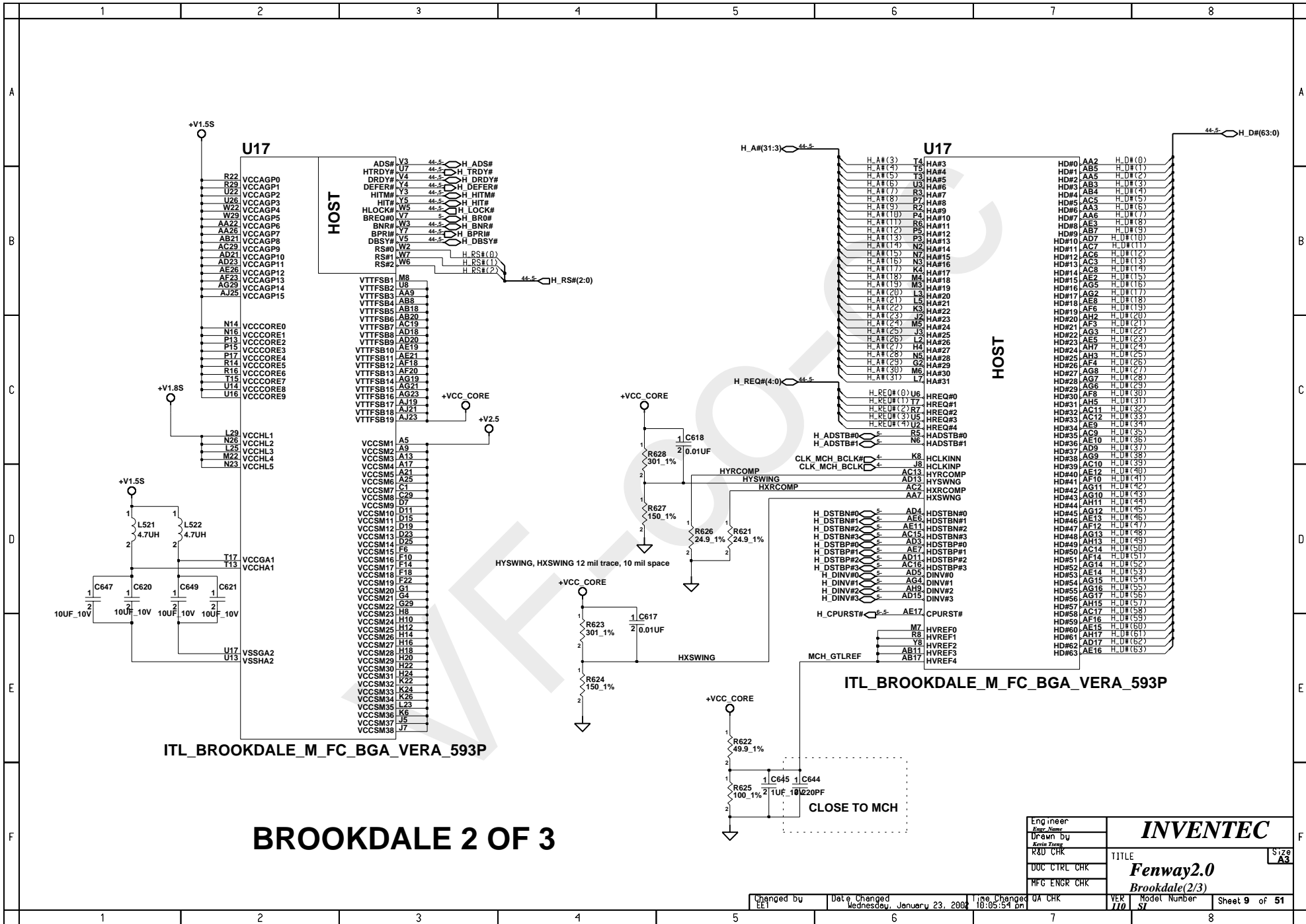
Engineer	
Drawn by	
Rev'd by	
Rev'd Date	
DOC CTRL	
PRG ENGR	
QA CHK	
VER	110
Model Number	SI
<b>INVENTEC</b>	
TITLE	
<b>Fenway2.0</b>	
Northwood(3/3)	
Sheet 7 of 51	

Changed by: EE1 Date Changed: Wednesday, January 23, 2008 Time Changed: 10:05:41 AM

Size A3

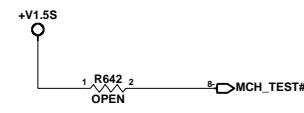
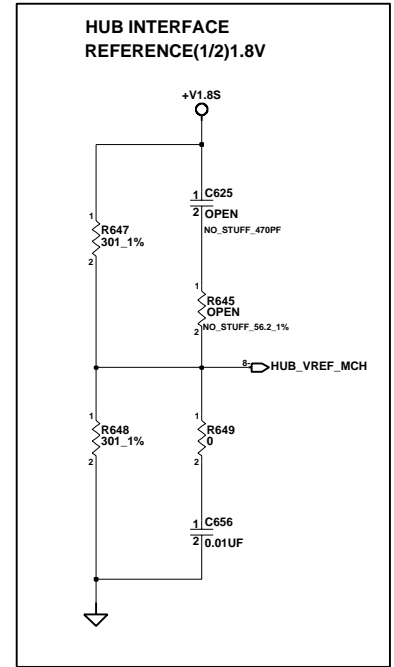
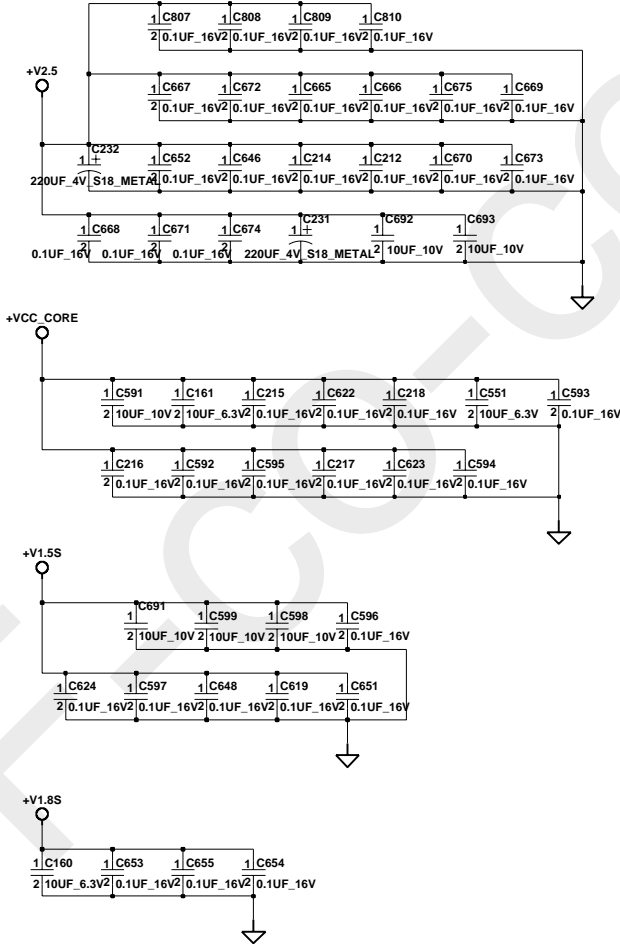
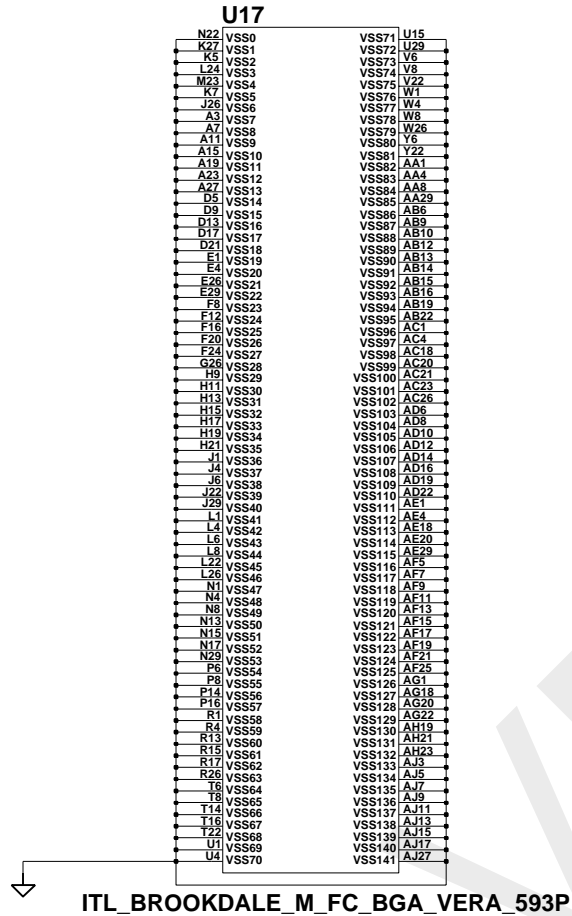






Eng Name		<b>INVENTEC</b>
Drawn by		
Rev'd		TITLE
R3D CHK		<b>Fenway2.0</b>
DOC CTRL CHK		<b>Brookdale(2/3)</b>
MFG ENGR CHK		Model Number
		VER <b>110</b>
		Sheet 9 of 51

Changed by: EE1 Date Changed: Wednesday, January 23, 2008 Time Changed: 10:05:54 pm

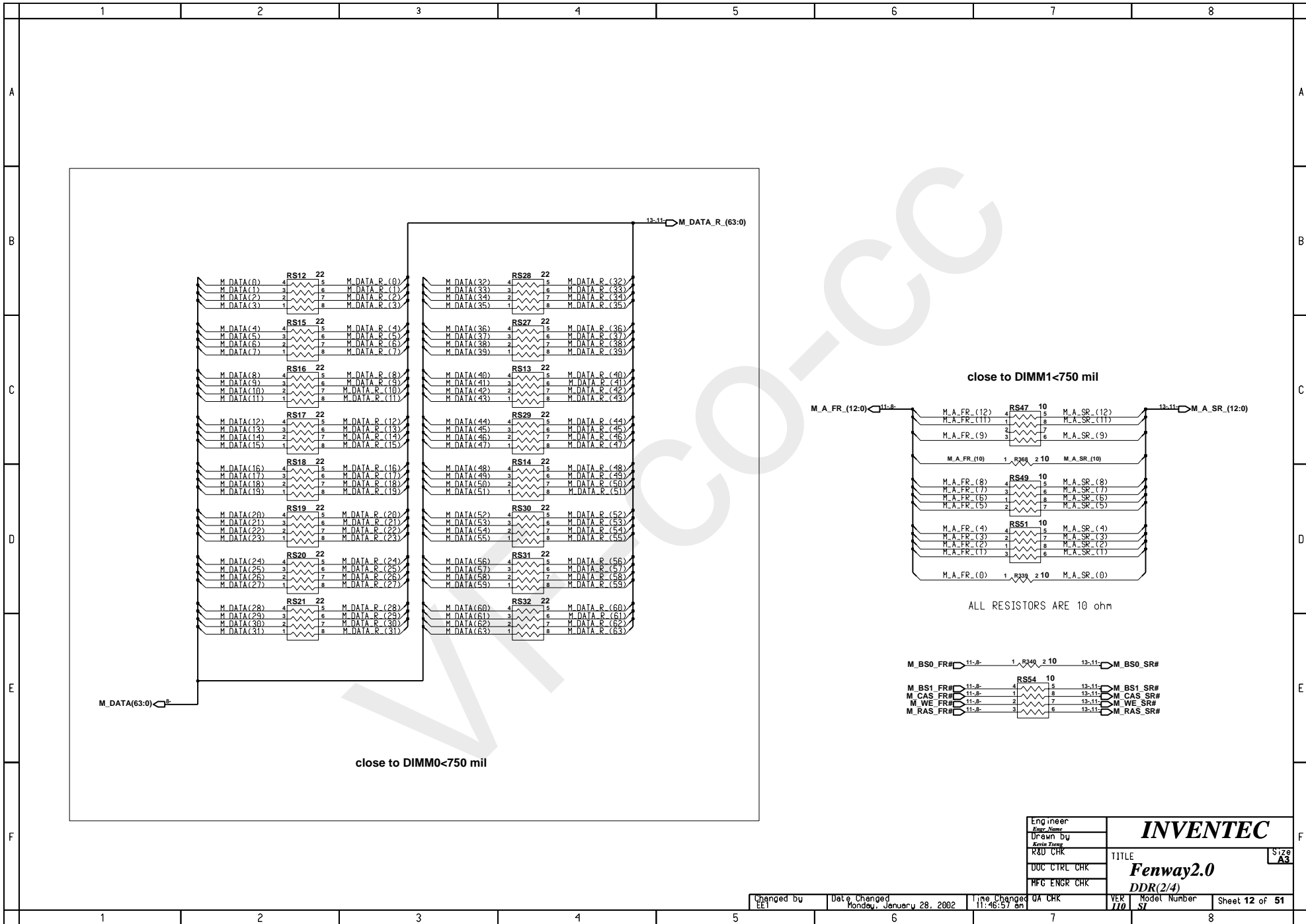


**BROOKDALE 3 OF 3**

Eng Ineer		<b>INVENTEC</b>	S178 A3		
Eng Name					
Drawn by					
Revise by					
R3D CHK					
DOC CTRL CHK	TITLE	<b>Fenway2.0</b>	Brookdale(3/3)		
MFG ENGR CHK	Model Number			VER 110	Sheet 10 of 51

Changed by	Date Changed	Eng Changed	UA CHK	VER	Model Number	Sheet
EE1	Friday, Nov 10, 2002	6:30:10 pm		110	SI	10 of 51



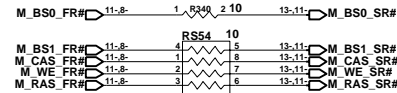


M\_DATA(63:0) ← B

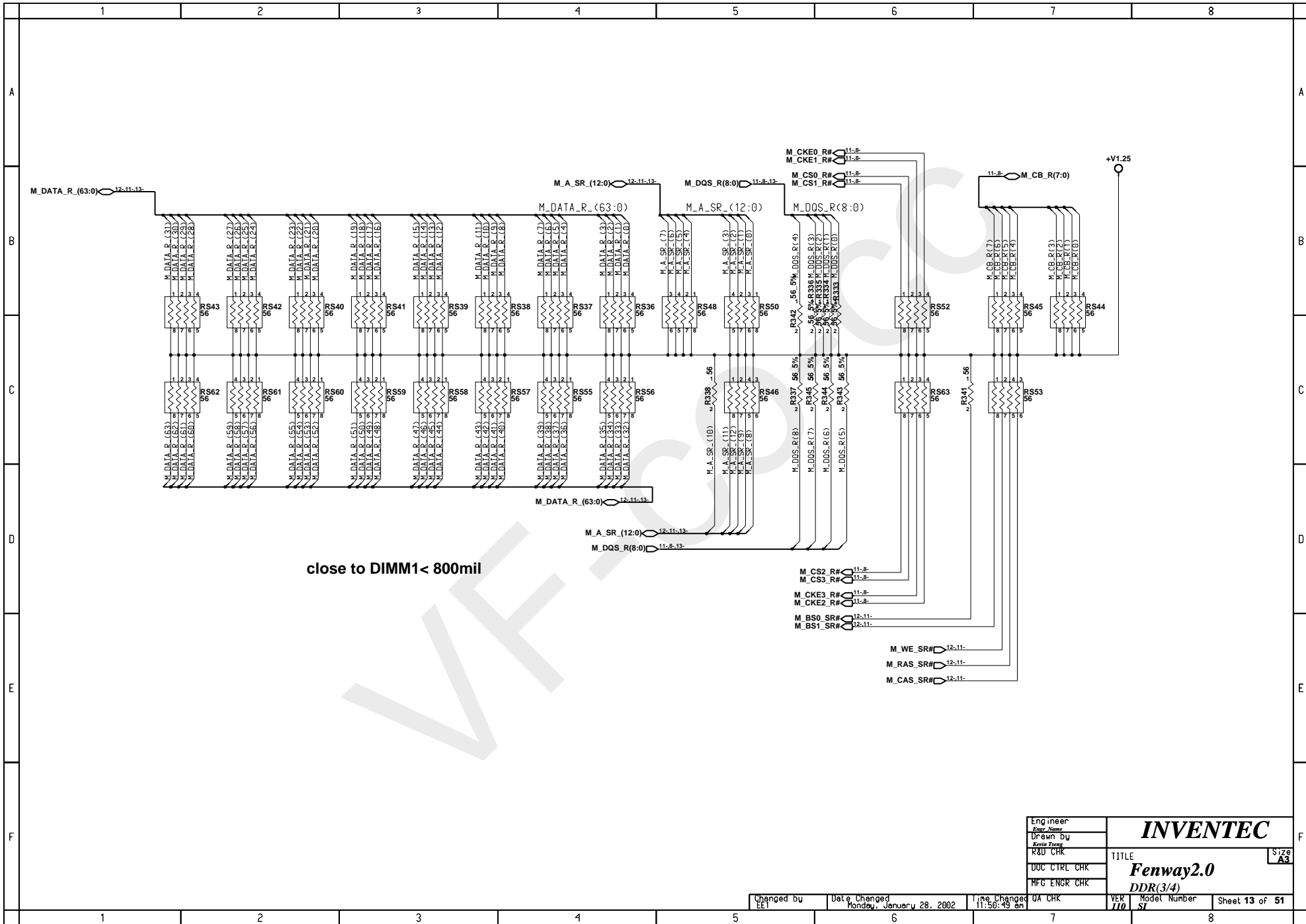
close to DIMM0<750 mil

close to DIMM1<750 mil

ALL RESISTORS ARE 10 ohm



Eng Ineer Engr Name	<b>INVENTEC</b>	S/29 A3
Drawn by Kevin Tseng		
R&D CHK		
DOC CTRL CHK		
HFG ENGR CHK		
TITLE	<b>Fenway2.0</b>	
Model Number	<b>DDR(2/4)</b>	



close to DIMM1 < 800mil

Engineer	
Drawn by	
Revise	
RDU CHK	
DOC CTRL CHK	
MFG ENGR CHK	
Model Number	VER 110
Sheet 13 of 51	

**INVENTEC**

TITLE  
**Fenway2.0**  
DDR(3/4)

S178  
A3

Changed by EE1 Date Changed Monday, January 28, 2002 Time Changed 11:50:49 am

A

B

C

D

E

F

A

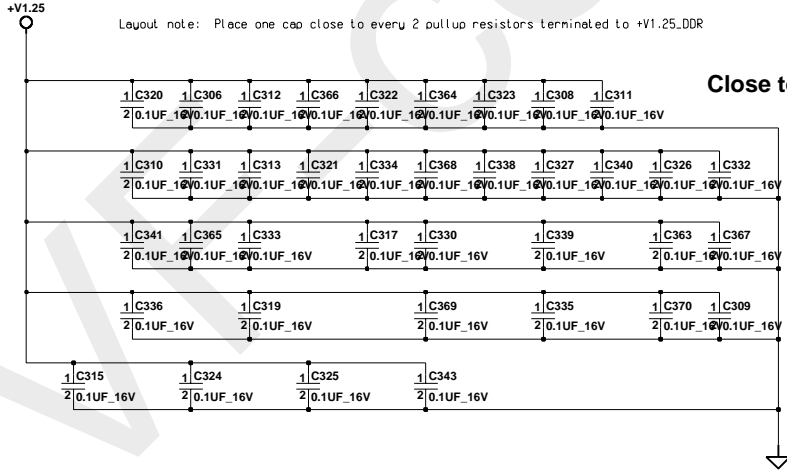
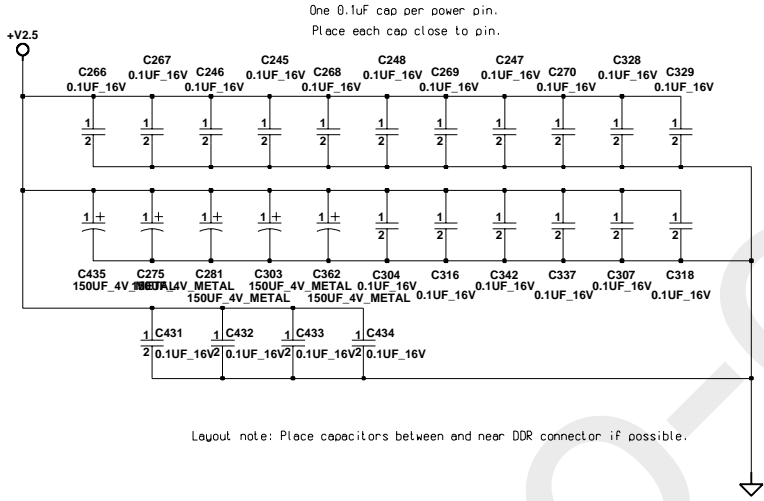
B

C

D

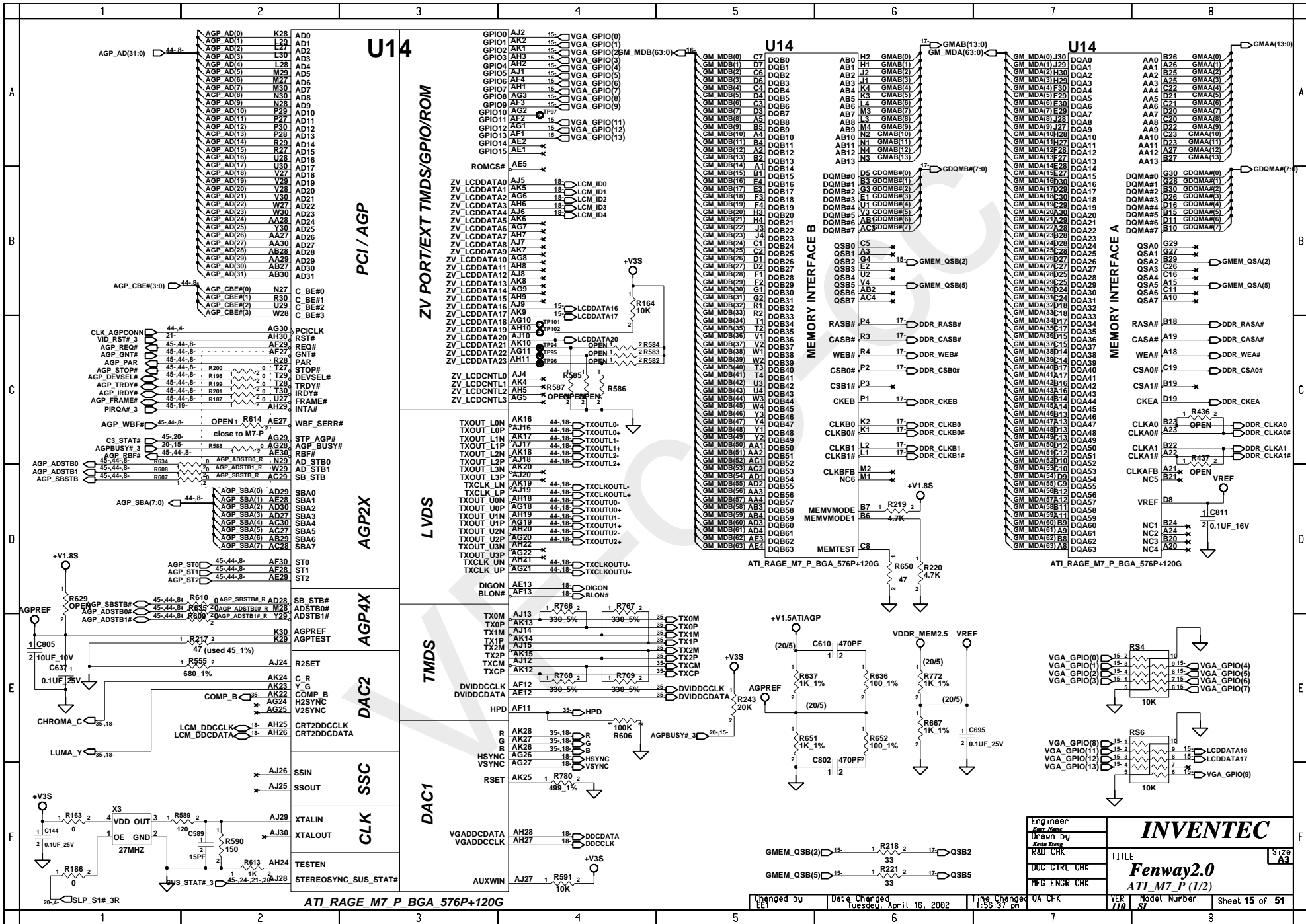
E

F



Eng. Name	<b>INVENTEC</b>	Size A3
Drawn by		
Rev. No.		
R3D CHK		
DOC CTRL CHK		
MFG ENGR CHK	TITLE	
	<b>Fenway2.0</b>	
	<b>DDR(4/4)</b>	
Changed by	Model Number	Sheet 14 of 51
EE1	110	51

Changed by: EE1  
Date Changed: Tuesday, April 16, 2002  
Time Changed: 1:38:04 pm



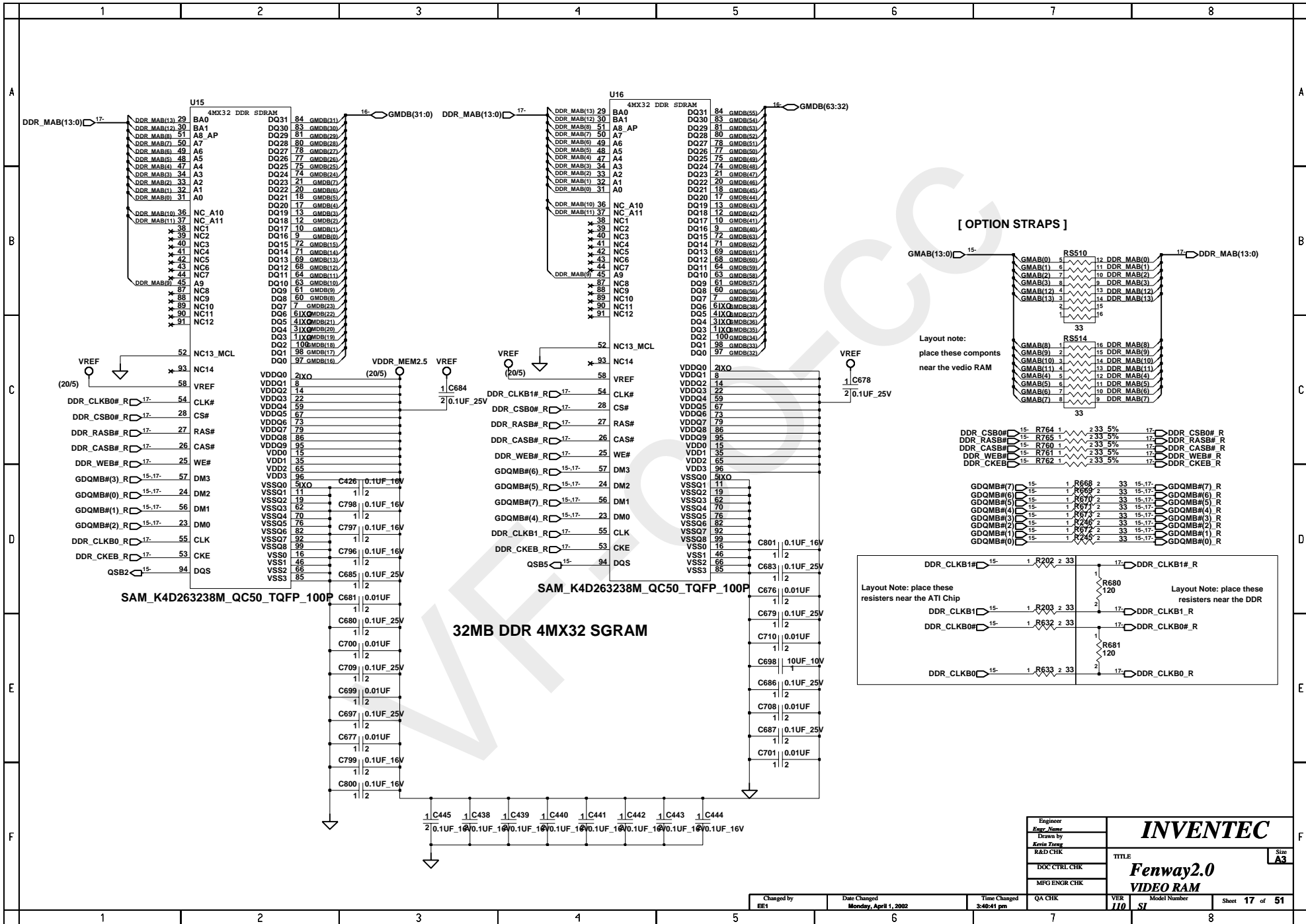
ATI\_RAGE\_M7\_P\_BGA\_576P+120G

Eng Ineer	Eng Name	Drawn By	Revise	Part No
<b>INVENTEC</b>				
<b>Fenway 2.0</b>				
<b>ATI M7 P (1/2)</b>				
DUO CTRL CHK	TITLE	VER	Model Number	Sheet 15 of 51
MFG ENGR CHK		110	SI	

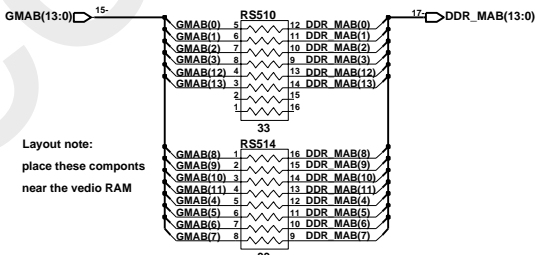
Changed by EET Date Changed Tuesday, Apr 16, 2002 Time Changed 1:56:37 pm UA CHK



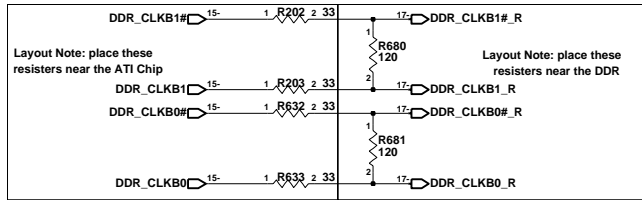
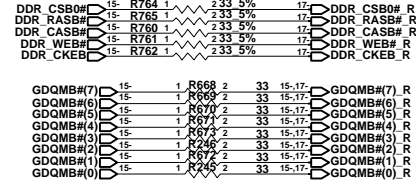




**[ OPTION STRAPS ]**



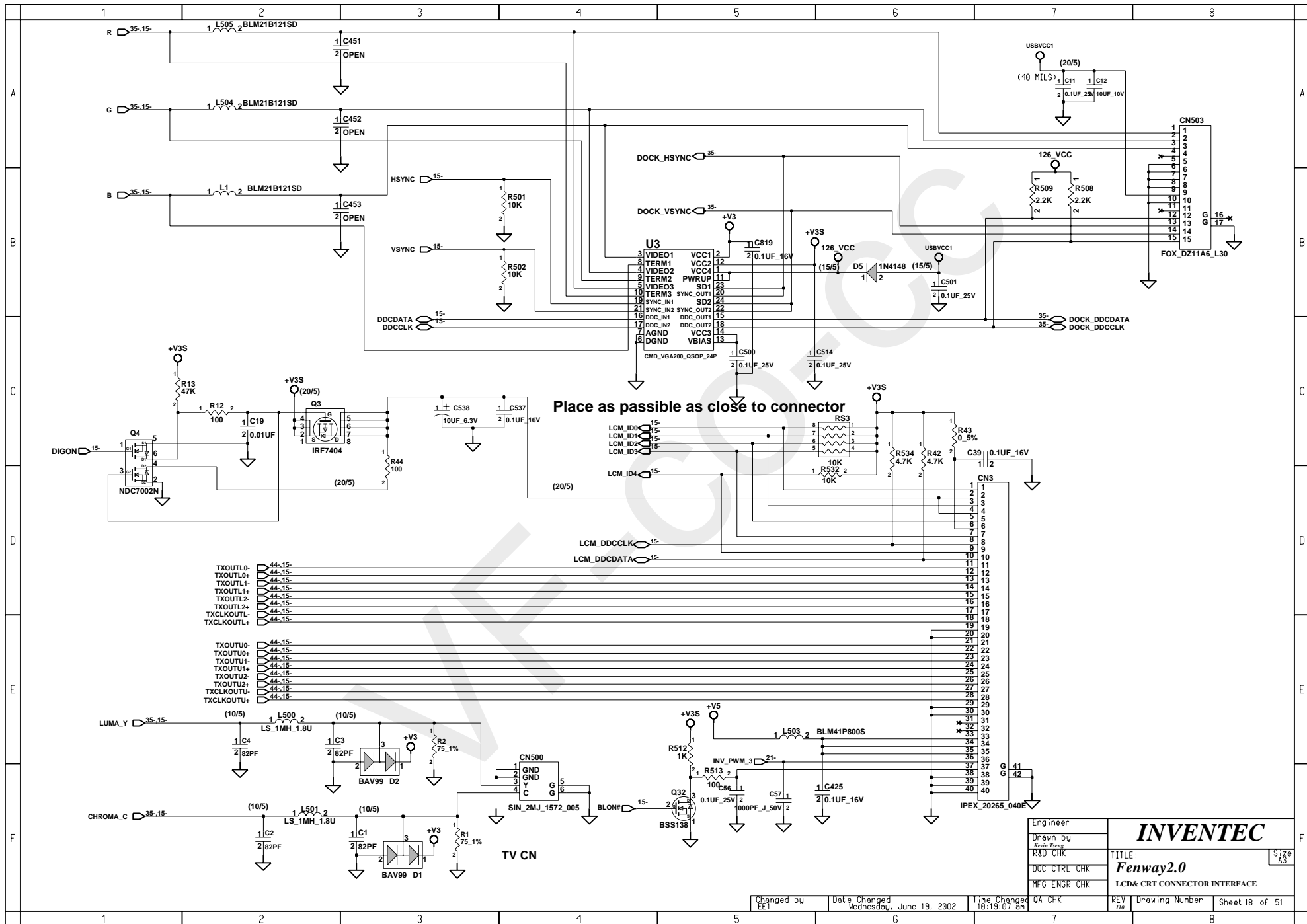
Layout note:  
place these componts  
near the vedio RAM



Layout Note: place these  
registers near the ATI Chip

Layout Note: place these  
registers near the DDR

Engineer		<b>INVENTEC</b>	Size <b>A3</b>
Engr Name			
Drawn by			
Revise			
R&D CHK			
TITLE		<b>Fenway2.0</b>	
DOC CTRL CHK		<b>VIDEO RAM</b>	
MFG ENGR CHK			
Changed by	Date Changed	Time Changed	QA CHK
EE1	Monday, April 1, 2002	3:40:41 pm	
VER	Model Number	Sheet 17 of 51	
110	SI		



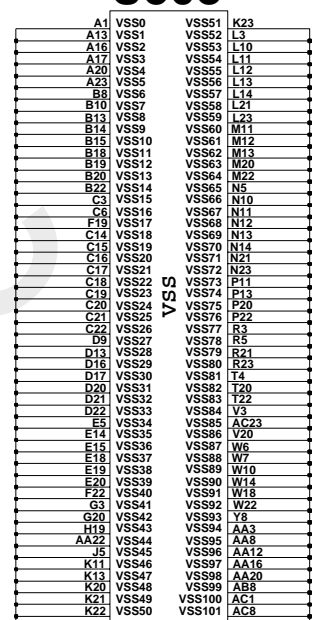
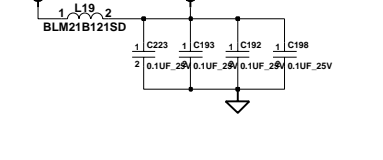
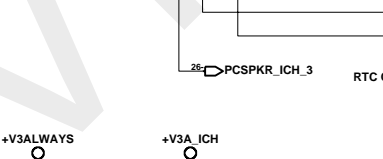
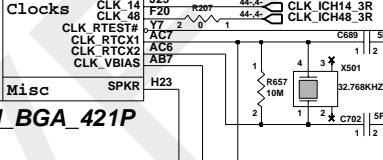
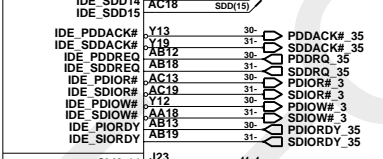
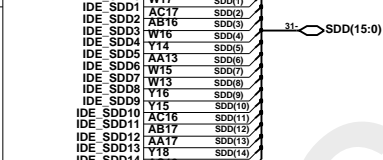
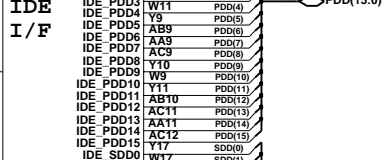
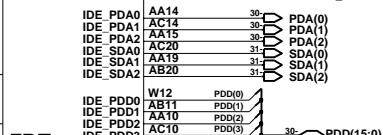
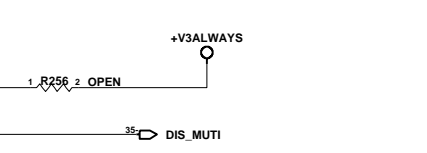
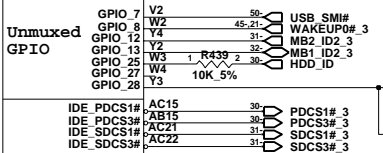
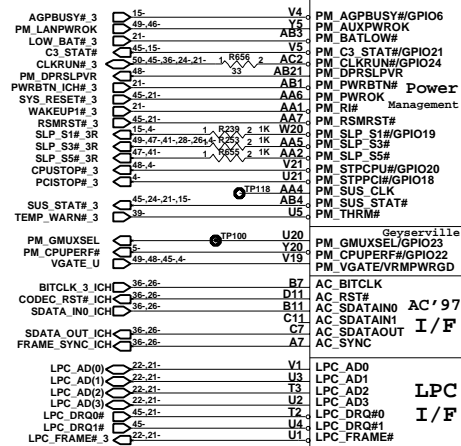
Place as possible as close to connector

Engineer		<b>INVENTEC</b> TITLE: <b>Fenway2.0</b> LCD & CRT CONNECTOR INTERFACE	Sheet 18 of 51
Drawn by			
Rev'd by			
DOC CTRL CHK			
MFG ENGR CHK			
Changed by	Date Changed	Time Changed	REV
EE1	Wednesday, June 19, 2002	10:19:07 am	110

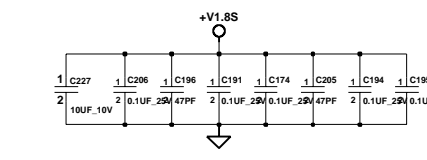
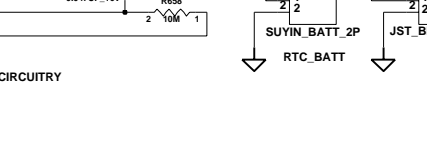
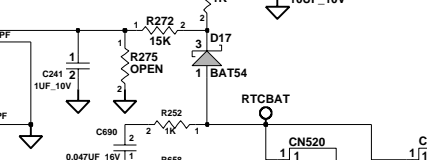
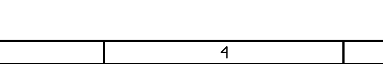
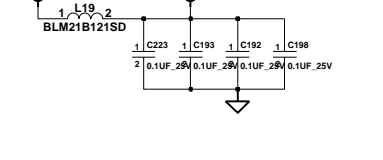
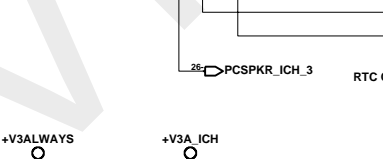
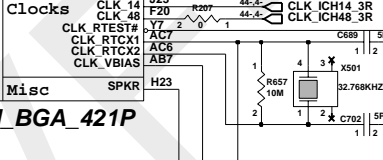
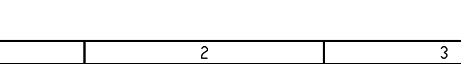
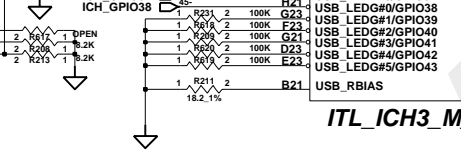
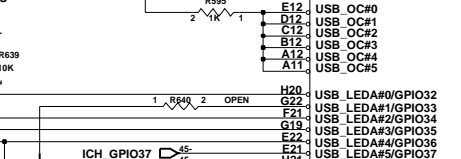
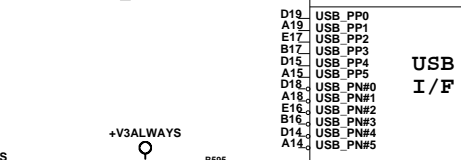


# U508

# U508



## ITL ICH3 M\_BGA\_421P



VMEM_CFG(2:0)-Memory type	
001	Samsung 32MB
011	Hynix 32MB
101	Micron 32MB
111	Winbond 32MB

Eng Iner	
Drawn by	
Revise	
R3D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

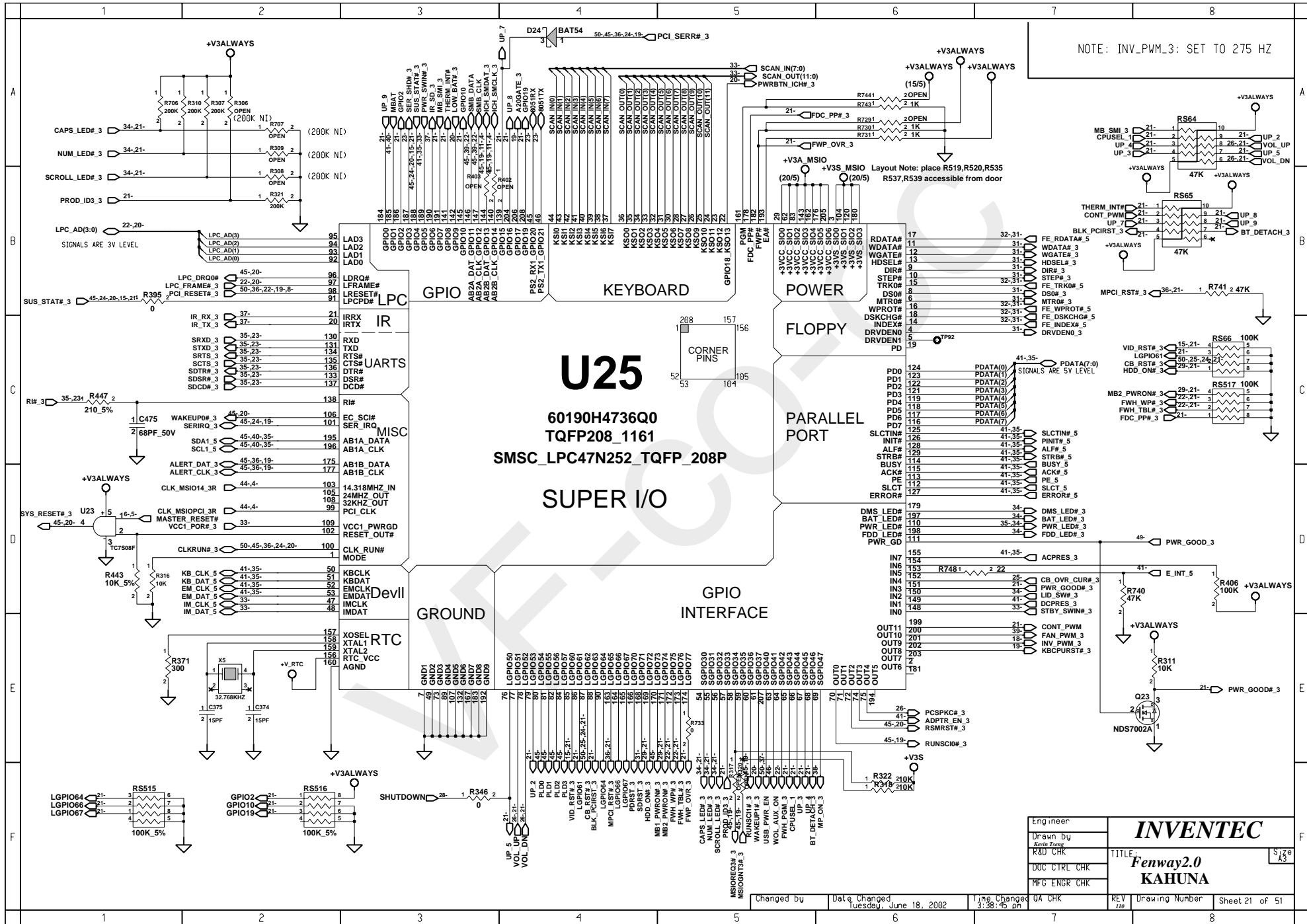
**INVENTEC**

TITLE **Fenway2.0**

Model Number **ICH3 (2/2)**

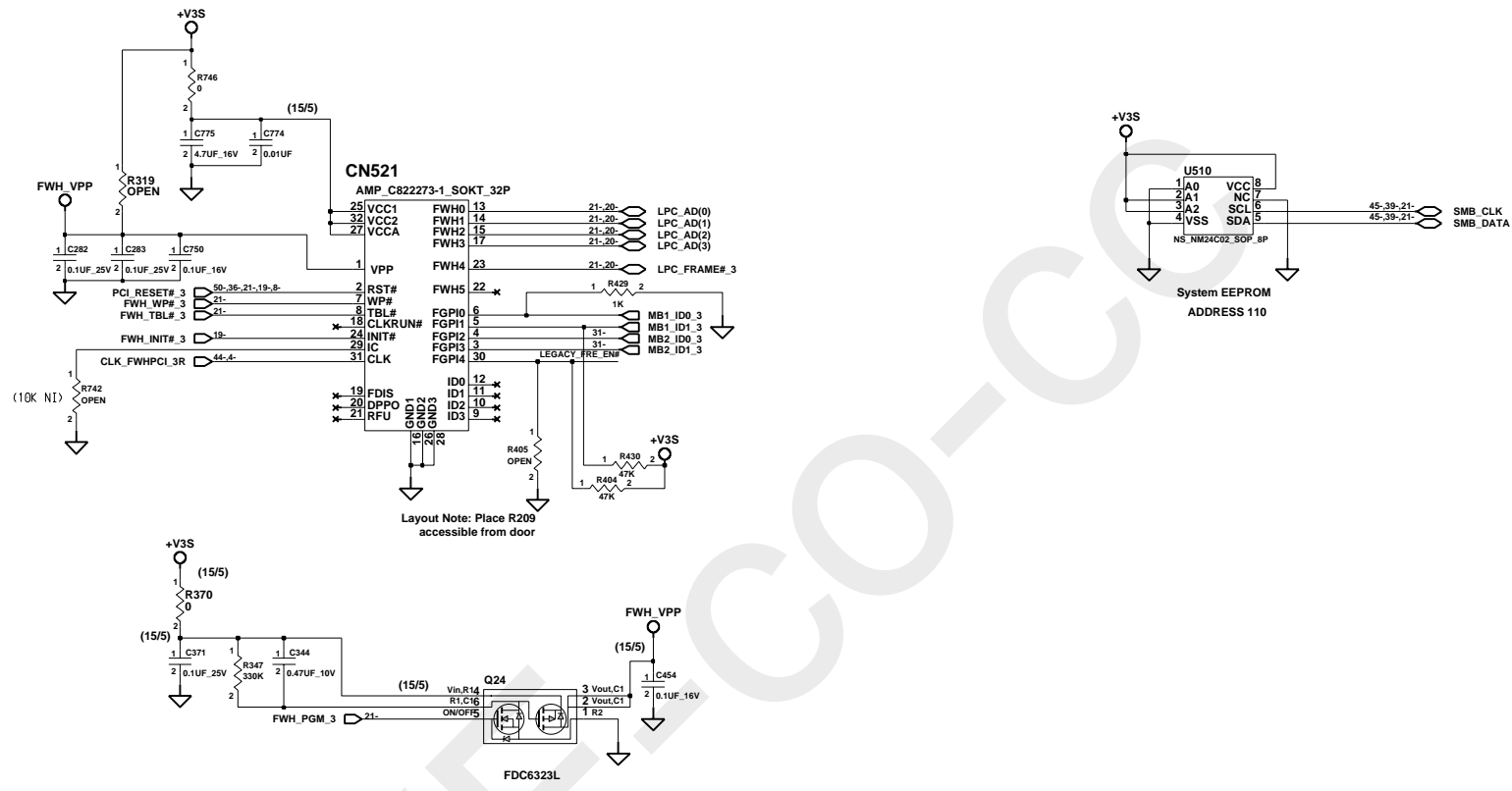
Sheet 20 of 51

Changed by EE1 Date Changed Saturday, March 30, 2002 Eng Changed 5:39:51 pm UA CHK



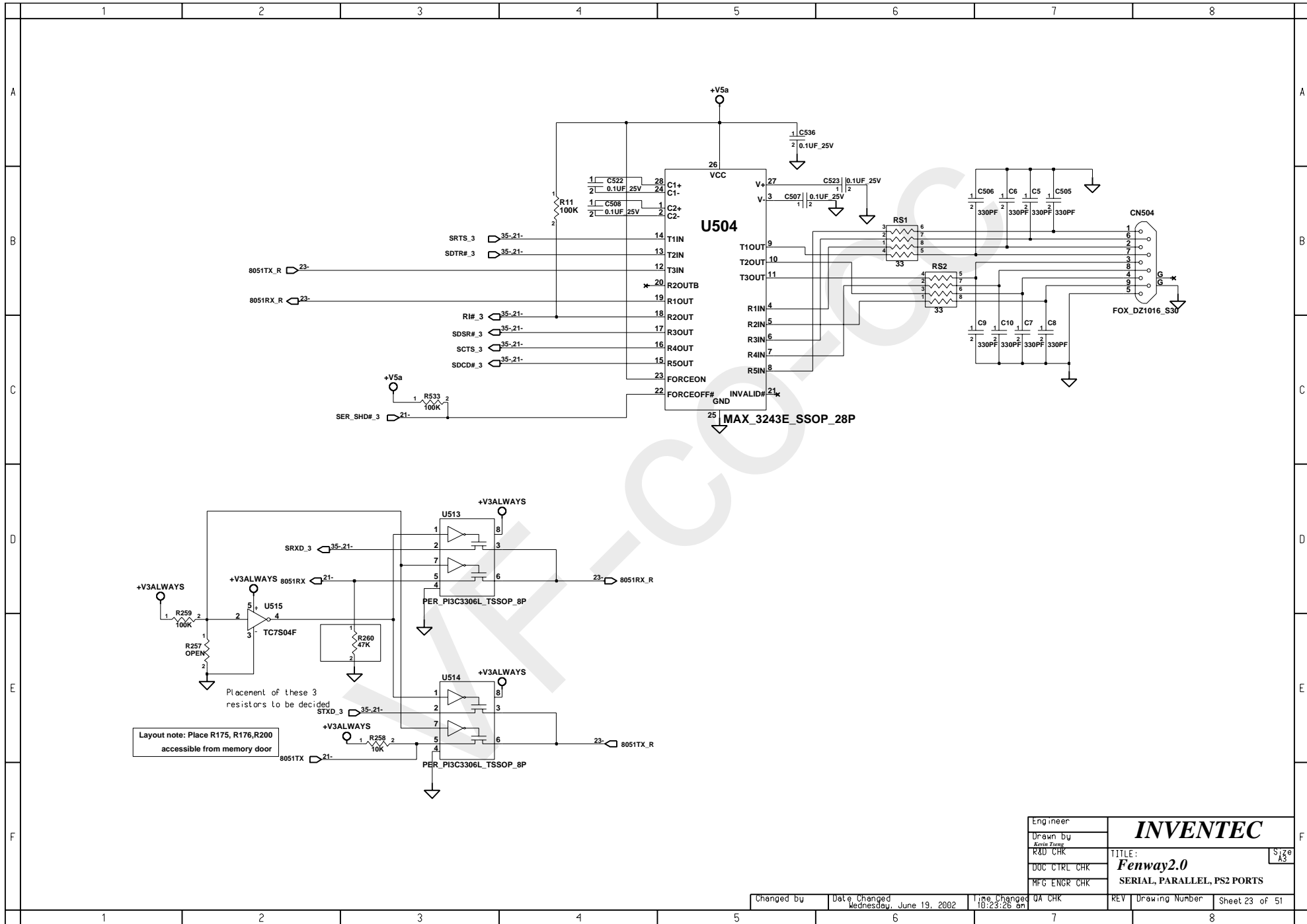
Eng'neer	
Drawn by	
RDV CHK	
DOC CTRL CHK	
MFG ENGR CHK	
REV 110	
DATE	June 18, 2002
TIME	3:38:45 pm
UA CHK	
REV 110	
DRAWING NUMBER	
SHEET	21 of 51

**INVENTEC**  
TITLE: **Fenway2.0**  
**KAHUNA**



Engineer	<b>INVENTEC</b>
Drawn by	
Rev'd by	
Rev'd by	
Rev'd by	
REV	TITLE: <b>Fenway2.0</b>
DOC CTRL CHK	S130
MFG ENGR CHK	<b>FWH&amp;EEPROM</b>

Changed by	Date Changed Tuesday, April 2, 2002	Time Changed 2:51:41 PM	QA CHK	REV	Drawing Number	Sheet 22 of 51
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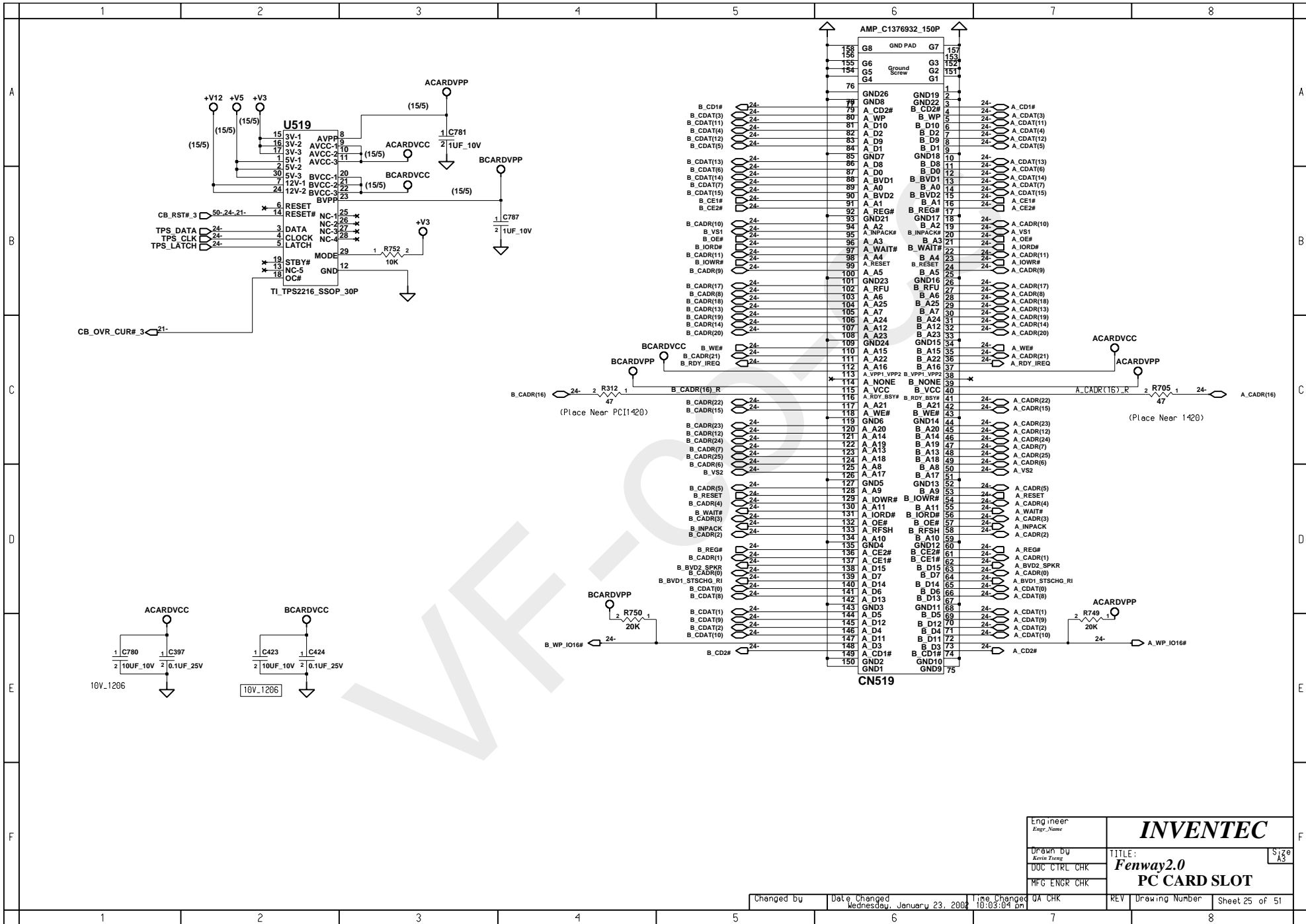


Engineer	<b>INVENTEC</b>	
Drawn by	Kevin Tseng	
RDY CHK	TITLE:	Size
DOC CTRL CHK	<b>Fenway2.0</b>	A3
MFG ENGR CHK	SERIAL, PARALLEL, PS2 PORTS	

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 23 of 51
	Wednesday, June 19, 2002	10:23:26 am				

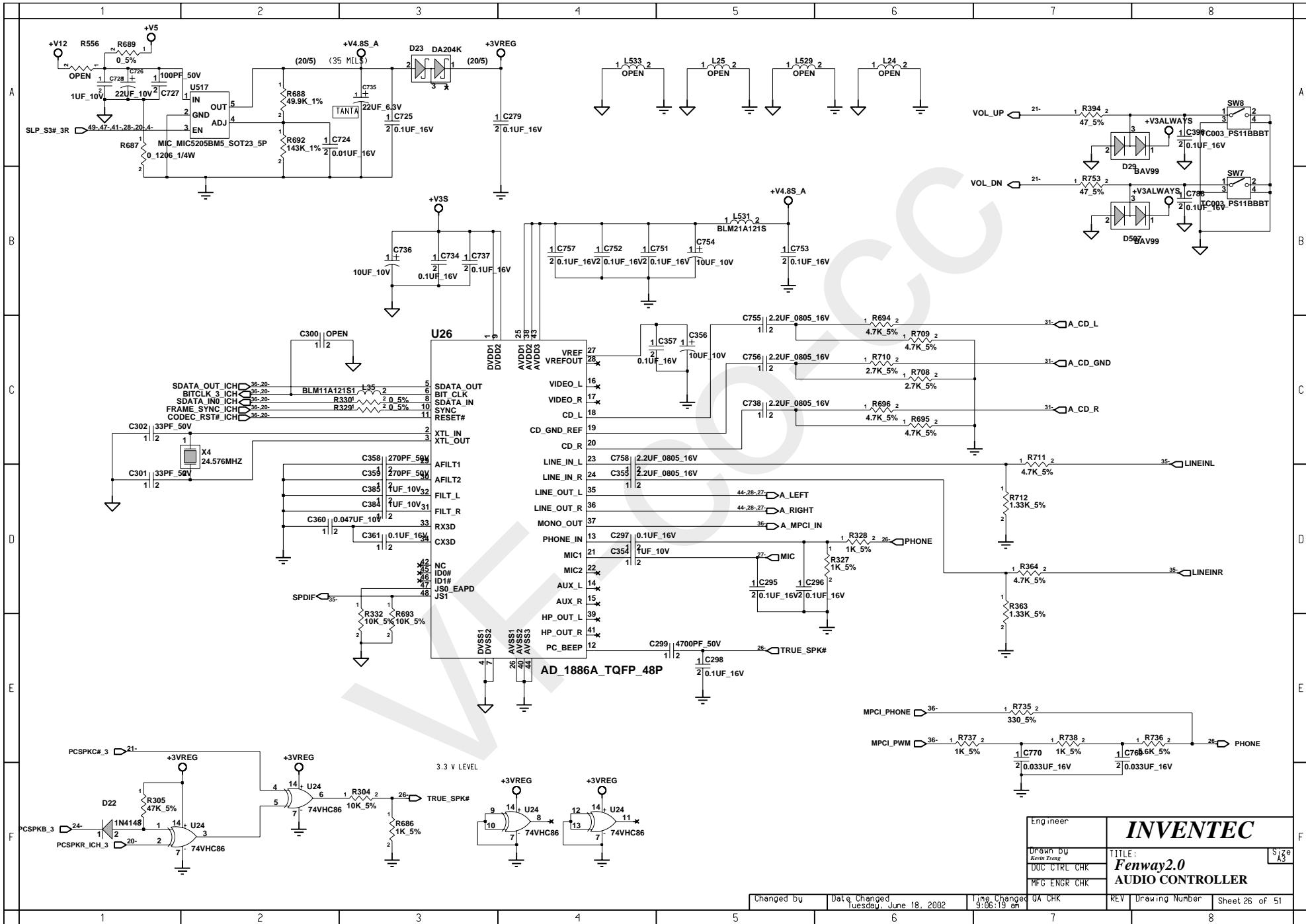






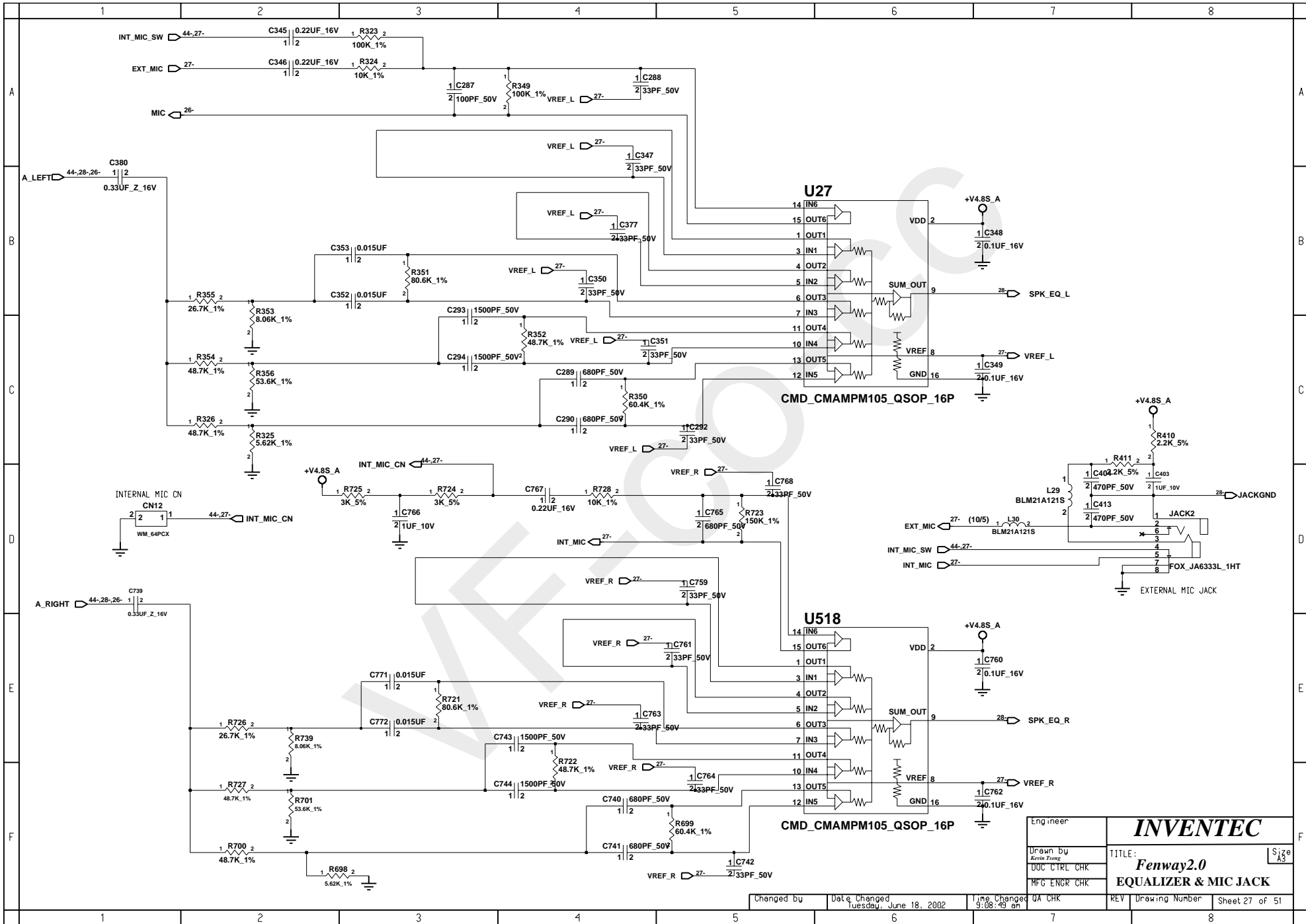
Eng Ineer Engr Name	<b>INVENTEC</b>		Size A3	
Drawn by Kevin Tseng				TITLE: <b>Fenway2.0</b>
DOC CTRL CHK				<b>PC CARD SLOT</b>
MFG ENGR CHK				

Changed by	Date Changed Wednesday, January 23, 2002	Time Changed 10:05:04 pm	REV	Drawing Number	Sheet 25 of 51
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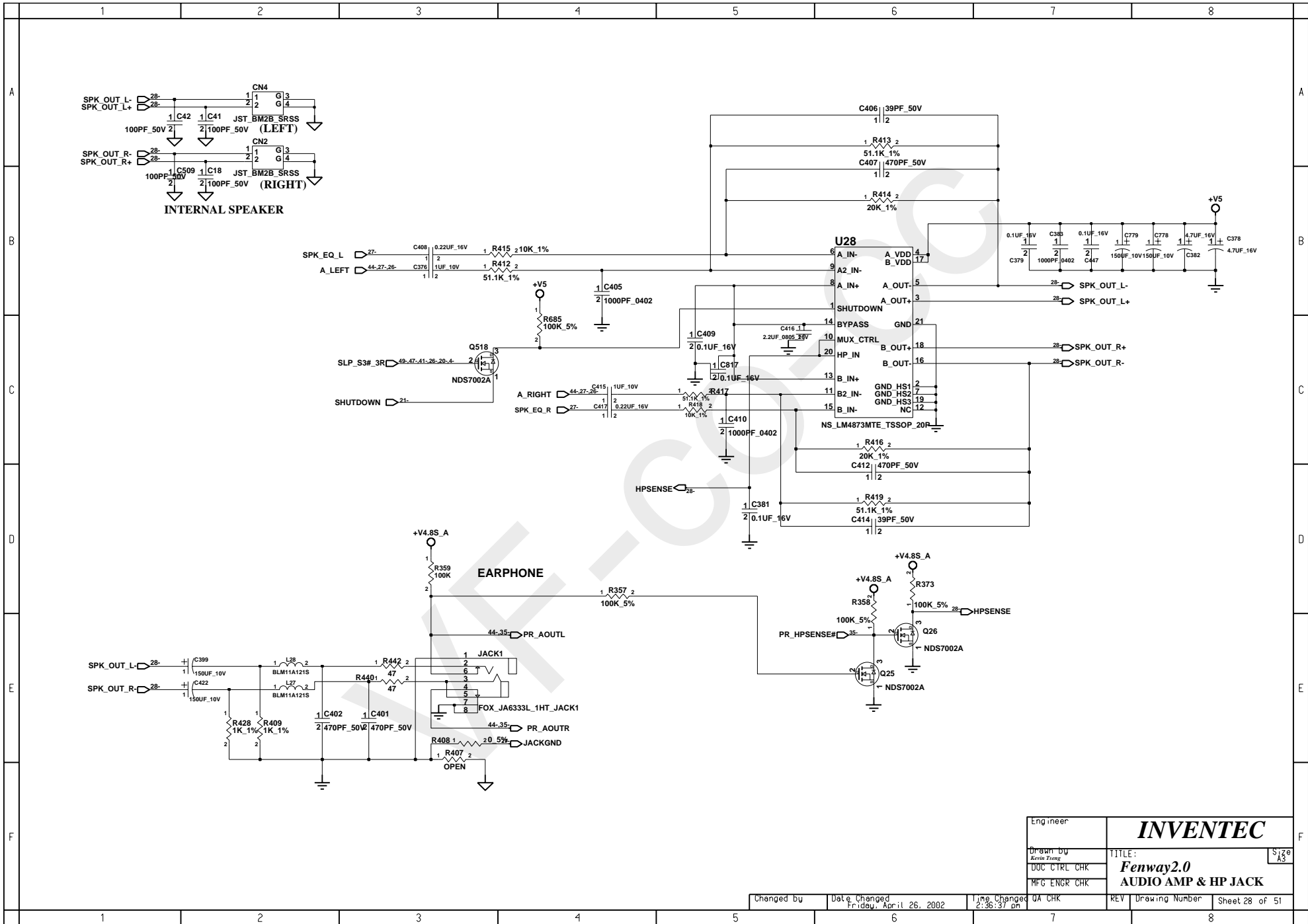


Eng ineer	<b>INVENTEC</b>	
Drawn by Kevin Tseng	TITLE: <b>Fenway2.0</b>	S178
DOC CTRL CHK	<b>AUDIO CONTROLLER</b>	
MFG ENGR CHK	REV	Sheet 26 of 51

Changed by	Date Changed Tuesday, June 18, 2002	Time Changed 9:06:19 am	UA CHK
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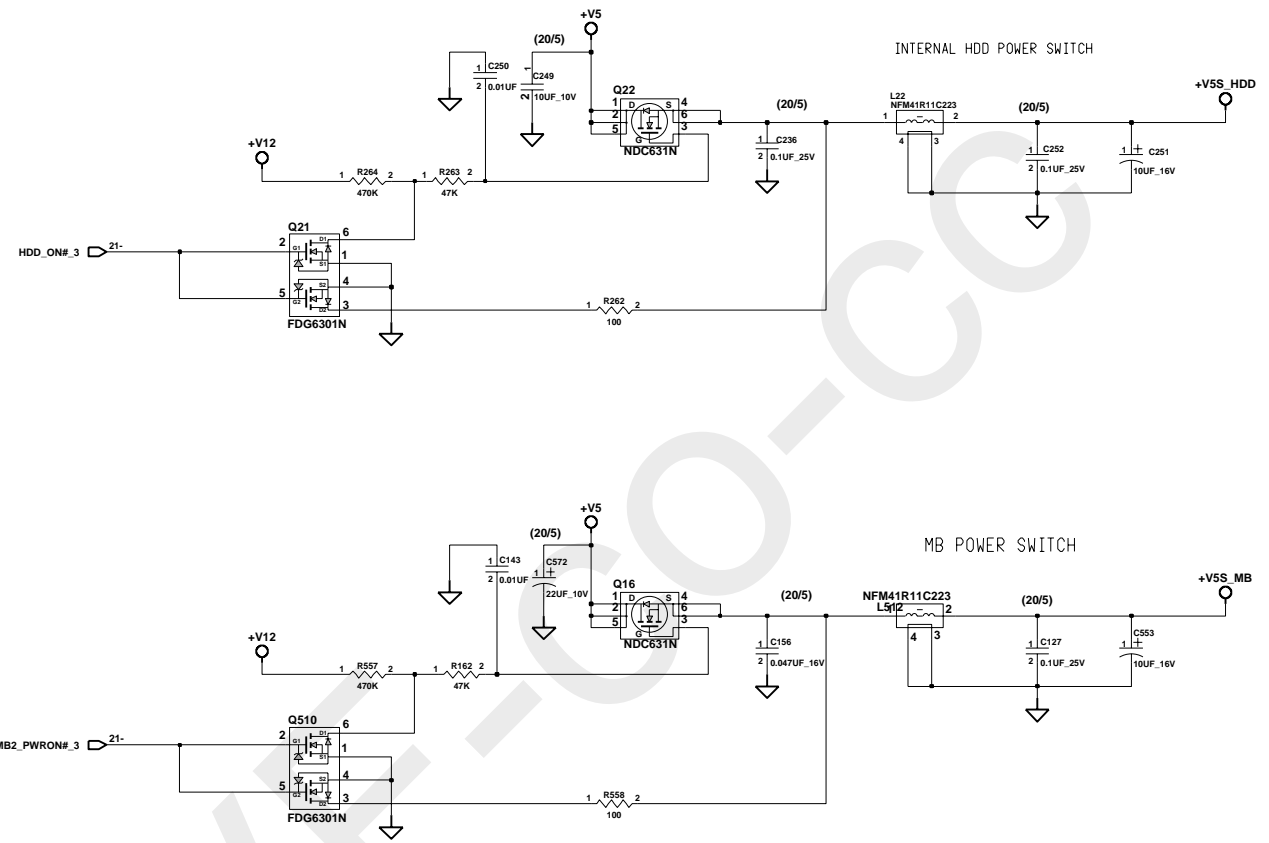


Engineer	<b>INVENTEC</b>	
Drawn by Kevin Tseng	TITLE: <b>Fenway2.0</b>	Size A3
DOC CTRL CHK	<b>EQUALIZER &amp; MIC JACK</b>	
MFG ENGR CHK	REV	Drawing Number
Changed by	Date Changed Tuesday, June 18, 2002	Time Changed 9:08:49 am
UA CHK	REV	Sheet 27 of 51



Engineer	<b>INVENTEC</b>	
Drawn by Kevin Tseng	TITLE: <b>Fenway2.0</b>	Size A3
DOC CTRL CHK	<b>AUDIO AMP &amp; HP JACK</b>	
MFG ENGR CHK		

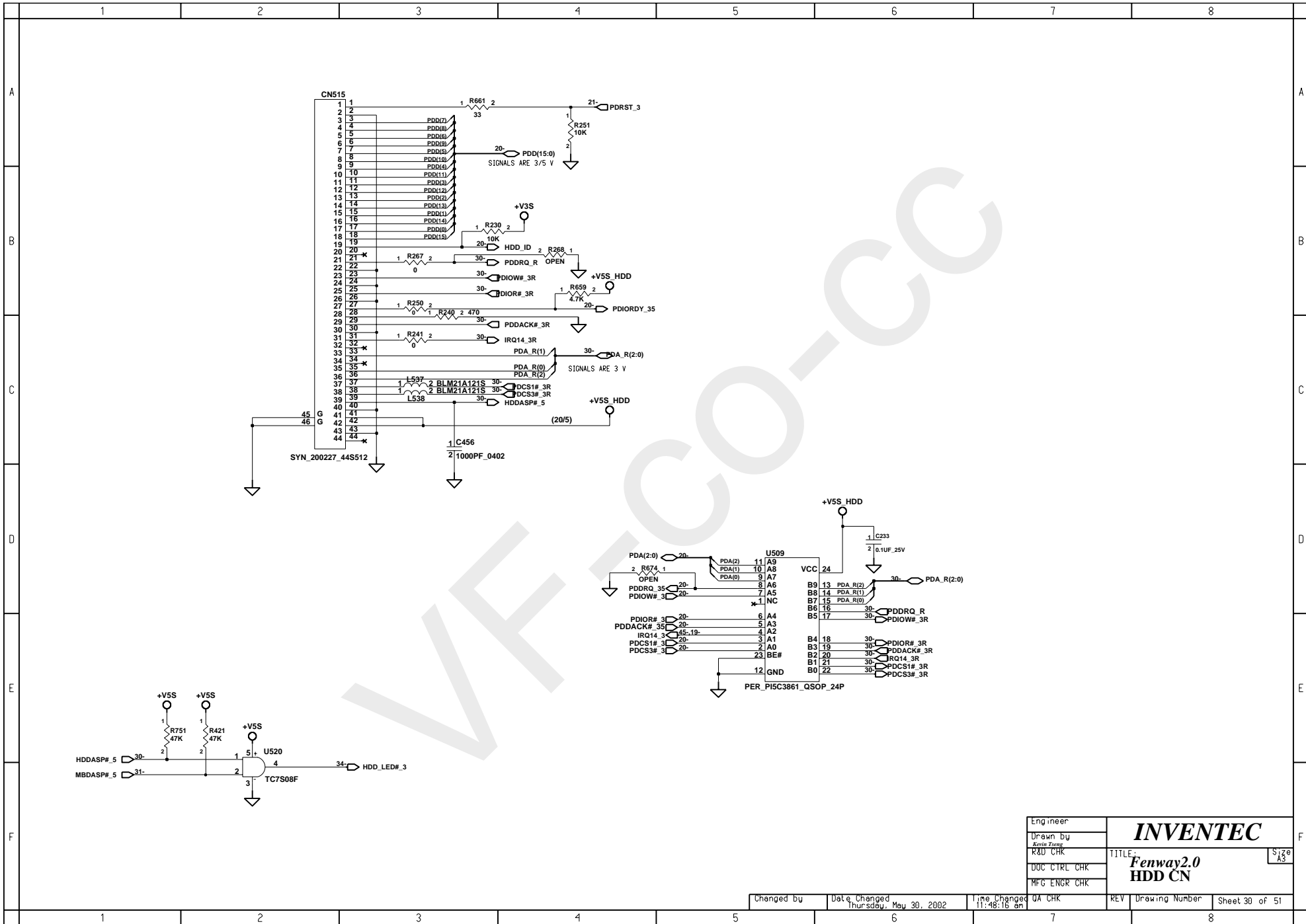
Changed by	Date Changed Friday, April 26, 2002	Time Changed 21:36:37 on	QA CHK	REV	Drawing Number	Sheet 28 of 51
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SIGNAL NAME	TRACE WIDTH (MILS)
+VSS_HDD	60
HDDPWR	60
+VSS_MB	60
MBPWR	60

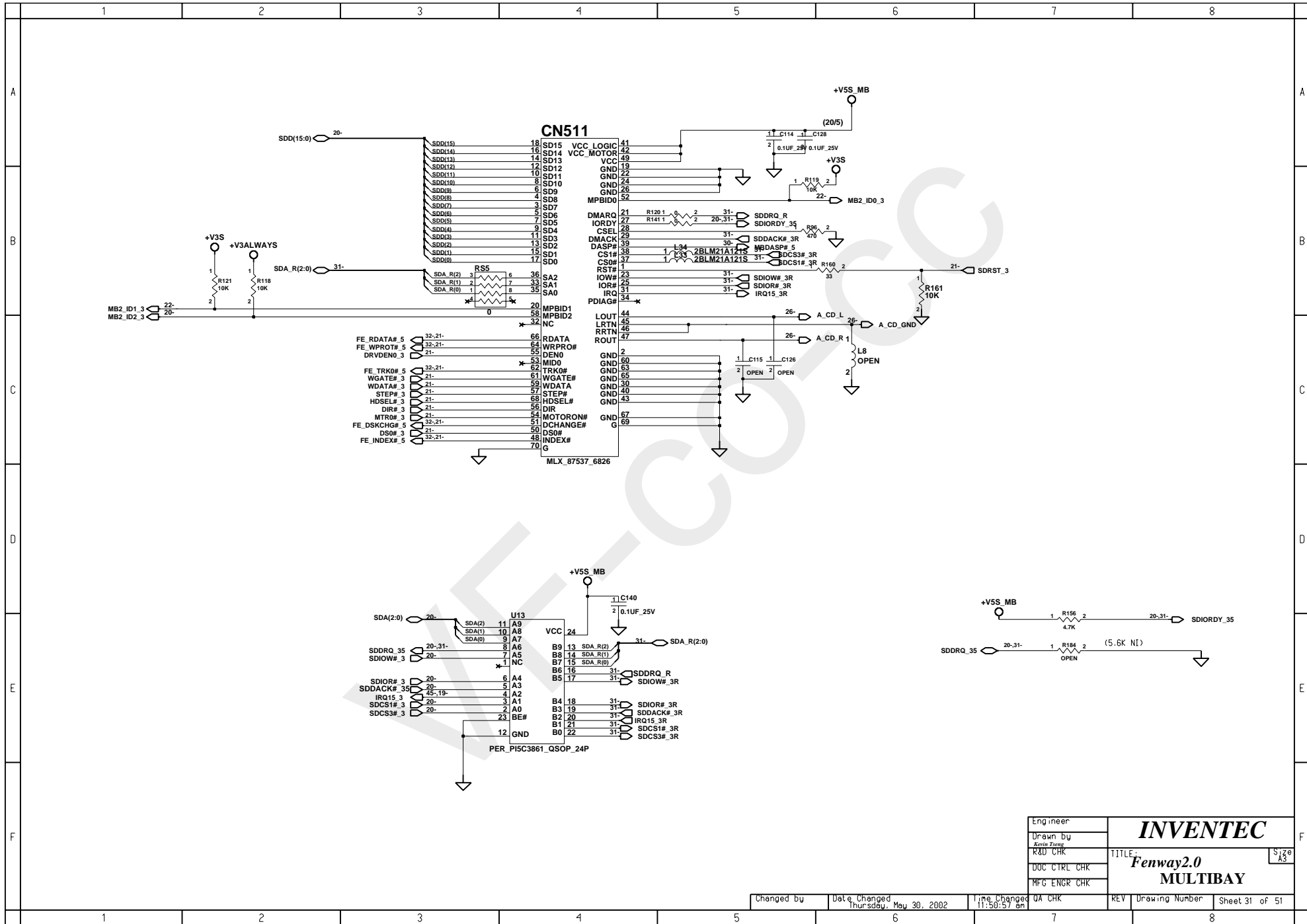
Engineer	<b>INVENTEC</b>		
Drawn by			
Rev'd by			
R&D CHK			TITLE: <b>Fenway2.0</b>
DOC CTRL CHK			HDD & MB PWR & SWITCH
MFG ENGR CHK	REV	Drawing Number	

Changed by: [ ] Date Changed: Wednesday, January 23, 2002 Time Changed: 10:05:24 pm



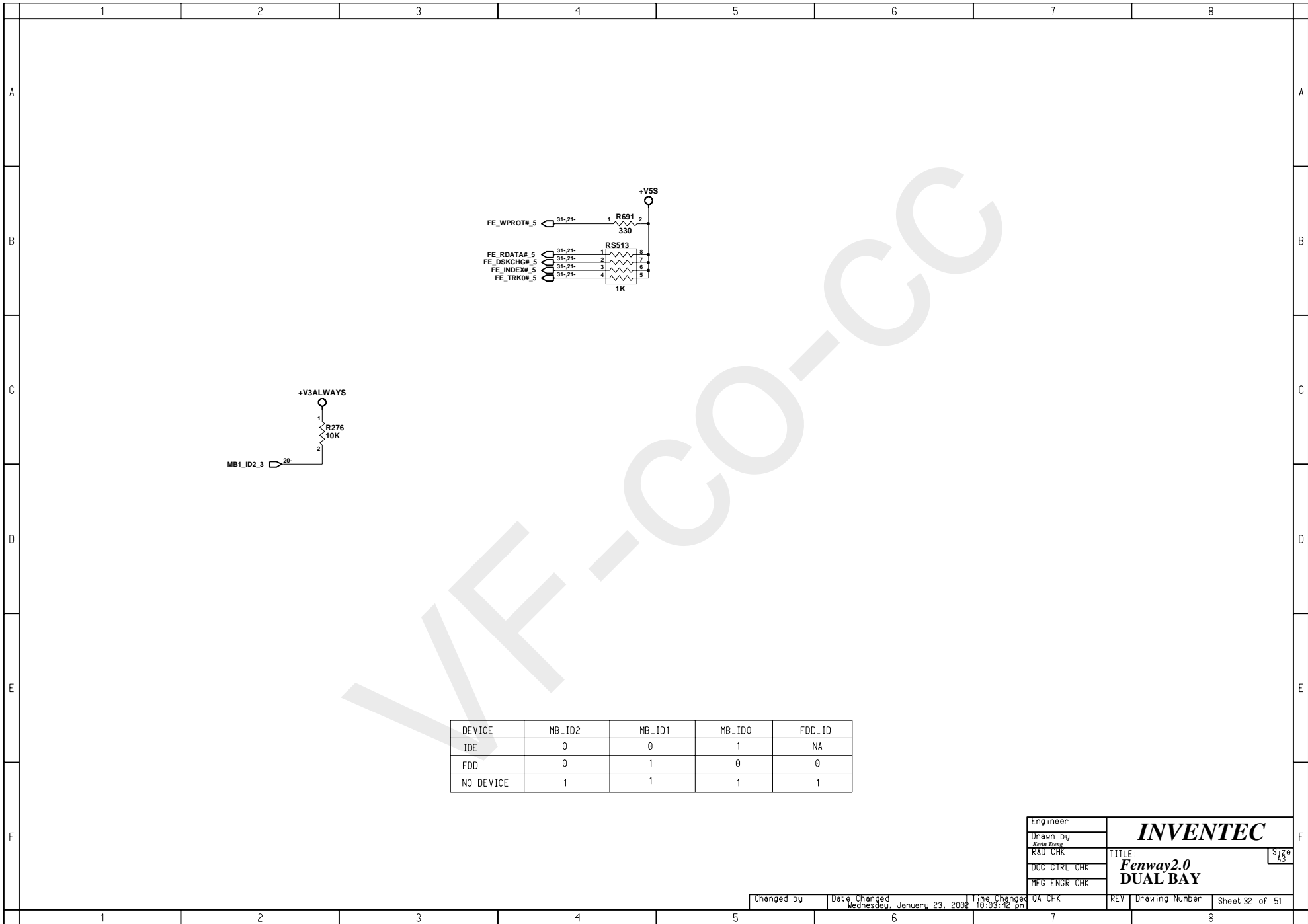
Engineer	<b>INVENTEC</b>		Size A3
Drawn by			
Rev'd			
DOC CTRL CHK			
MFG ENGR CHK			
TITLE	<b>Fenway2.0 HDD CN</b>		

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 30 of 51
	Tuesday, May 30, 2002	11:48:16 am				



Engineer	<b>INVENTEC</b>	
Drawn by		
Rev'd		
DOC CTRL		
MFG ENGR		
TITLE	<b>Fenway2.0</b>	
	<b>MULTIBAY</b>	
Size	A3	

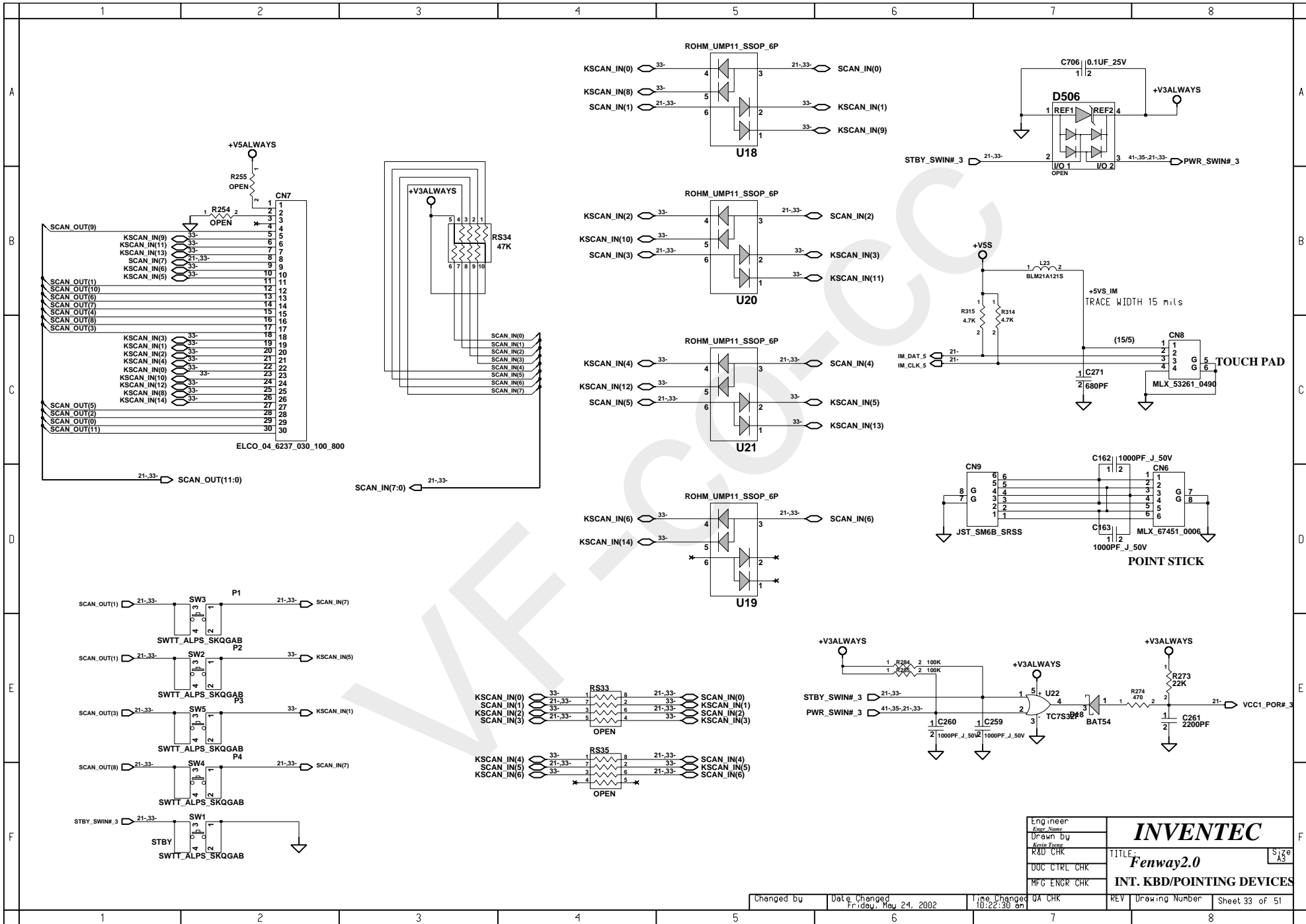
Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 31 of 51
	Thursday, Mar 30, 2002	11:50:57 am				



Engineer	<b>INVENTEC</b>
Drawn by	
Rev'd by	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	TITLE: <b>Fenway2.0 DUAL BAY</b> <span style="float: right;">Size A3</span>

Changed by	Date Changed	Time Changed	QA CHK	REV	Drawing Number	Sheet 32 of 51
	Wednesday, January 23, 2008	10:05:42 am				





Eng Ineer	
Engr Name	
Drawn by	
Reviz Tmng	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

**INVENTEC**  
 TITLE: Fenway2.0  
 INT. KBD/POINTING DEVICES

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 33 of 51
	Friday, May 24, 2002	10:22:30 am				

1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

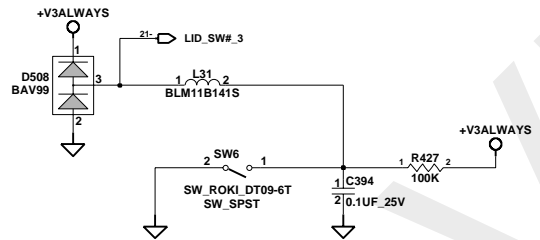
B

C

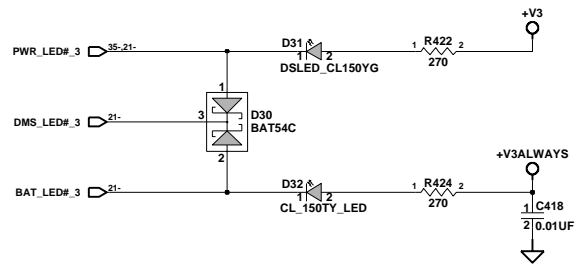
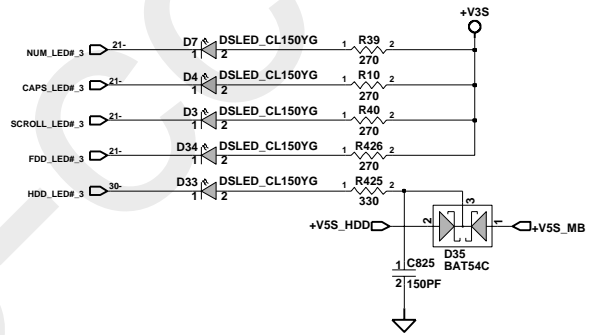
D

E

F



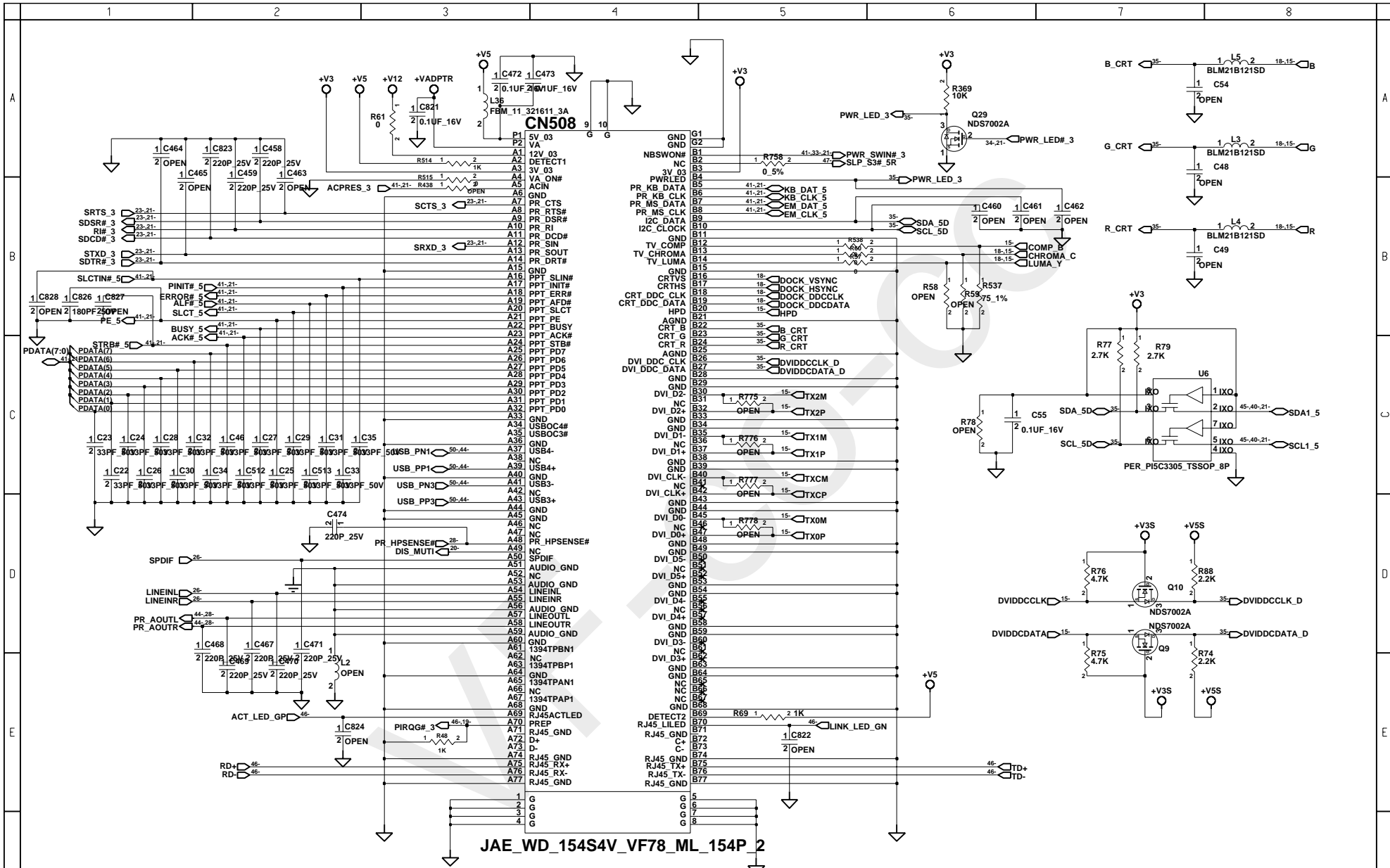
LID SWITCH



Engineer	<b>INVENTEC</b>	
Drawn by Kevin Tseng	TITLE: <b>Fenway2.0</b>	Size A3
DOC CTRL CHK	<b>LID SWITCHES / LEDS</b>	
MFG ENGR CHK		

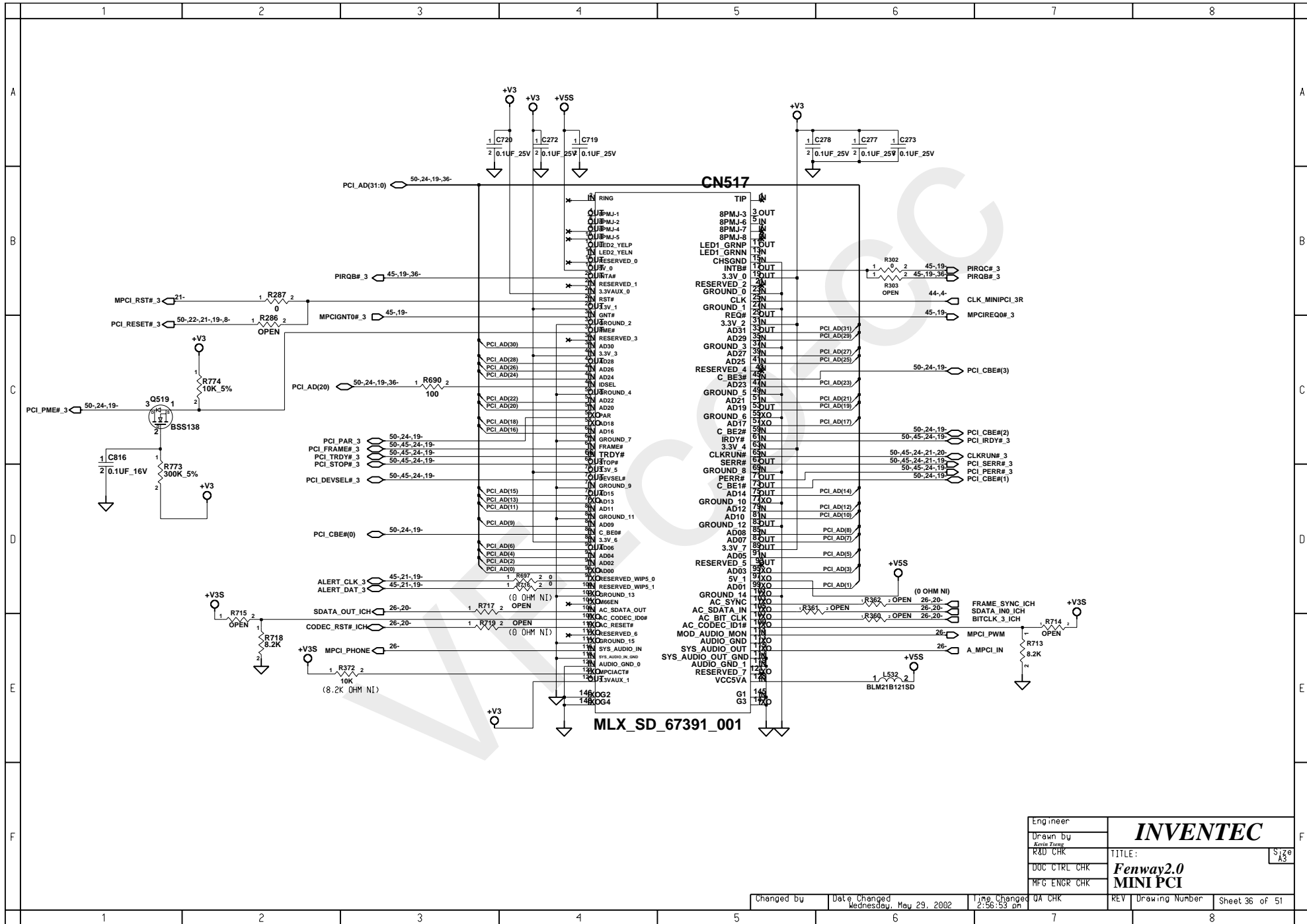
Changed by	Date Changed Thursday, May 30, 2002	Time Changed 6:33:31 pm	QA CHK	REV	Drawing Number	Sheet 34 of 51
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1 2 3 4 5 6 7 8



Engineer	<b>INVENTEC</b>	
Drawn by Kevin Tseng	TITLE: <b>Fenway2.0</b>	Size A3
DOC CTRL CHK	<b>DOCKING CONNECTOR</b>	
MFG ENGR CHK		

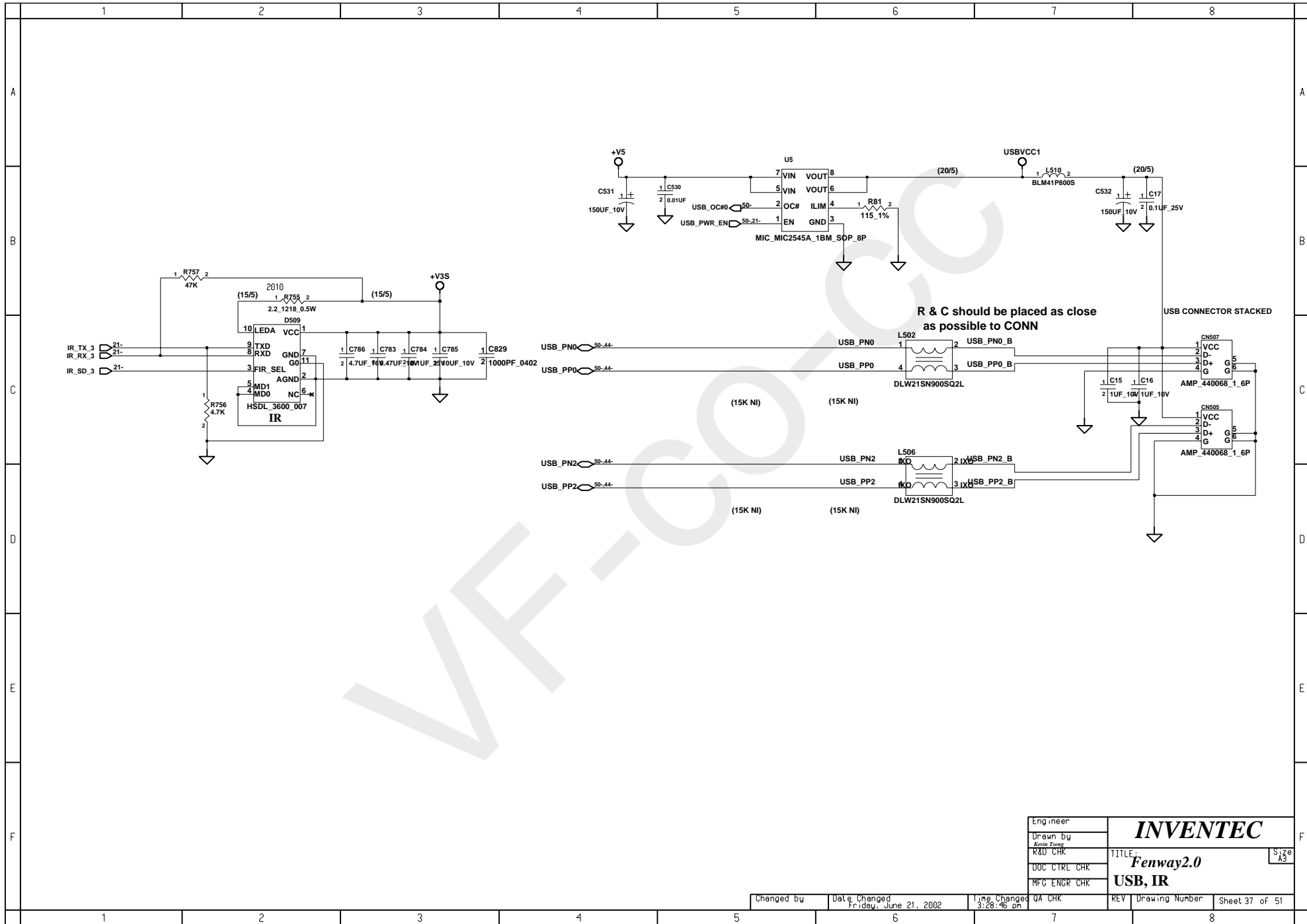
Changed by	Date Changed Tuesday, June 18, 2002	Time Changed 9:29:03 am	UA CHK	REV	Drawing Number	Sheet 35 of 51
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Eng Ineer	
Drawn by	
Rev'd	
DOC CTRL CHK	
MFG ENGR CHK	

<b>INVENTEC</b>	
TITLE:	S170
<b>Fenway2.0</b>	
<b>MINI PCI</b>	

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 36 of 51
	Wednesday, May 29, 2002	2:56:53 pm				



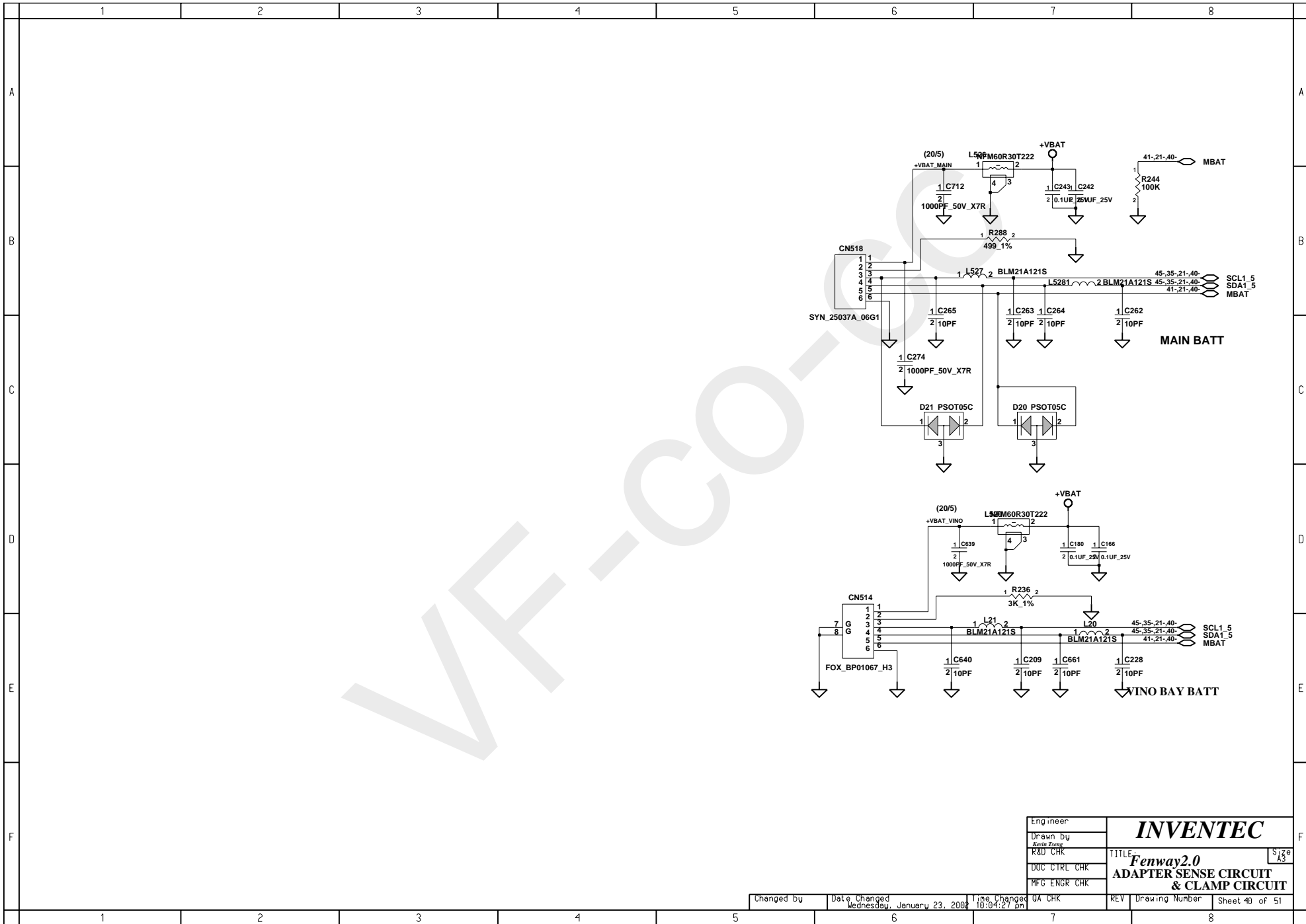
R & C should be placed as close as possible to CONN

Engineer	<b>INVENTEC</b> TITLE: <b>Fenway2.0</b> <b>USB, IR</b>		Size
Drawn by			AS
Rev'd			
RDY CHK			
DOC CTRL CHK			
MFG ENGR CHK			

Changed by	Date Changed	Time Changed	UA CHK	REV	Drawing Number	Sheet 37 of 51
	Friday, June 21, 2002	3:28:46 pm				



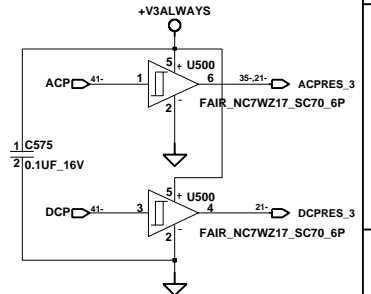
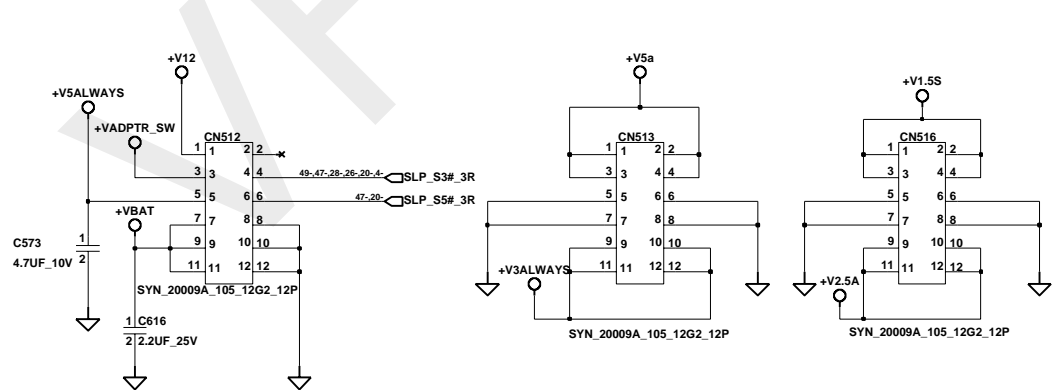
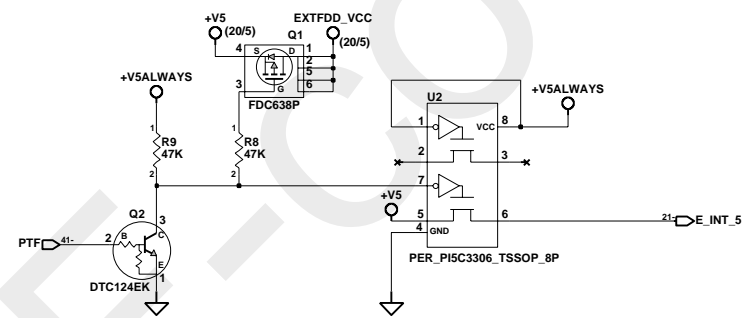
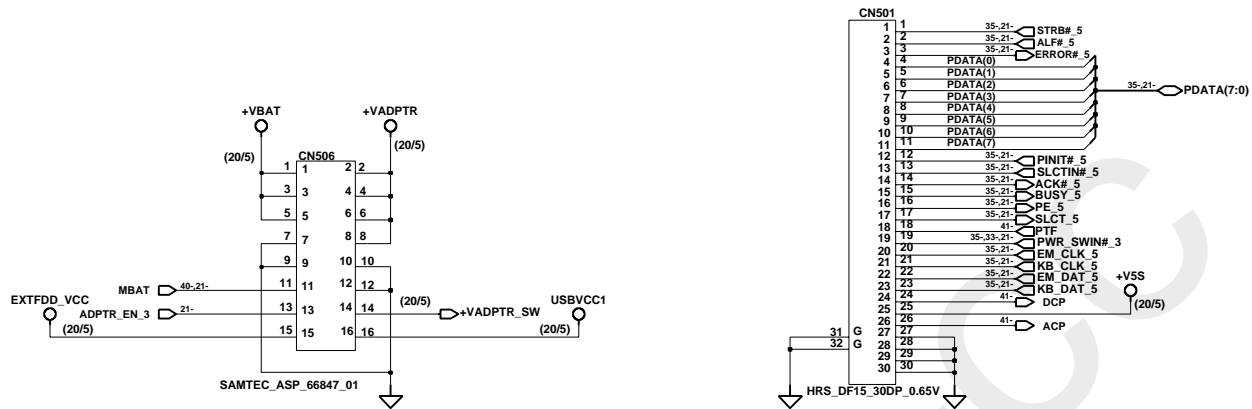




Engineer	<b>INVENTEC</b>	
Drawn by	Kevin Tseng	
RDV CHK	UA CHK	
DOC CTRL CHK	MFG ENGR CHK	
TITLE: <b>Fenway2.0 ADAPTER SENSE CIRCUIT &amp; CLAMP CIRCUIT</b>		Size: 3.5

Changed by	Date Changed	Time Changed	REV	Drawing Number	Sheet 40 of 51
	Wednesday, January 23, 2008	10:04:27 am	UA CHK		

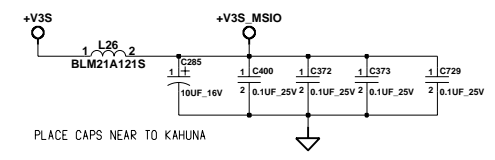




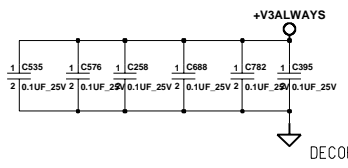
**POWER BOARD CN**

Engineer CHAI SONG POH	<b>INVENTEC</b>	
Drawn by KING KONG		
DOC CTRL CHK	TITLE: <b>Fenway2.0</b>	Size A3
MFG ENGR CHK	<b>PWR MODULE CONN</b>	
Changed by UA CHK	Date Changed Wednesday, April 17, 2002	Time Changed 10:15:25 am
REV	Drawing Number	Sheet 41 of 51

A

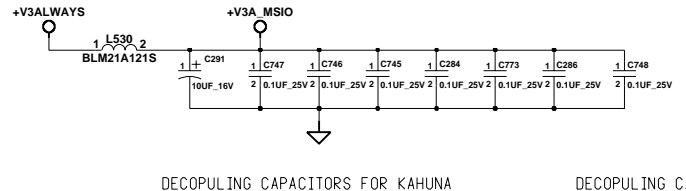


PLACE CAPS NEAR TO KAHUNA

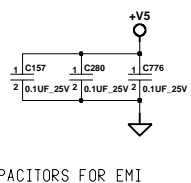


DECOPULING CAPACITORS FOR EMI

B

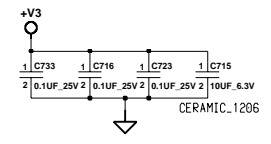


DECOPULING CAPACITORS FOR KAHUNA



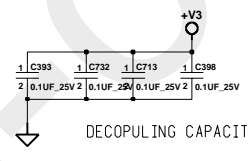
DECOPULING CAPACITORS FOR EMI

C



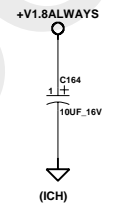
DECOPULING CAPS FOR CARDBUS CONTROLLER

CERAMIC\_1206

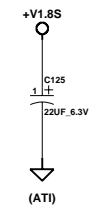


DECOPULING CAPACITORS FOR EMI

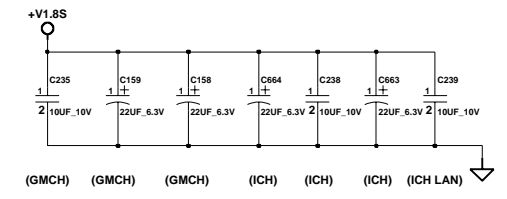
D



(ICH)

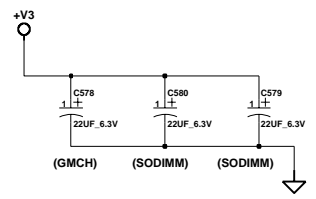


(ATI)

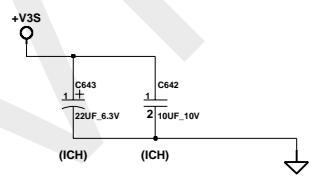


(GMCH) (GMCH) (GMCH) (ICH) (ICH) (ICH) (ICH LAN)

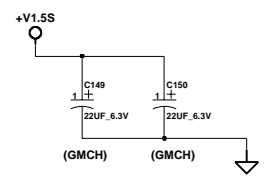
E



(GMCH) (SODIMM) (SODIMM)



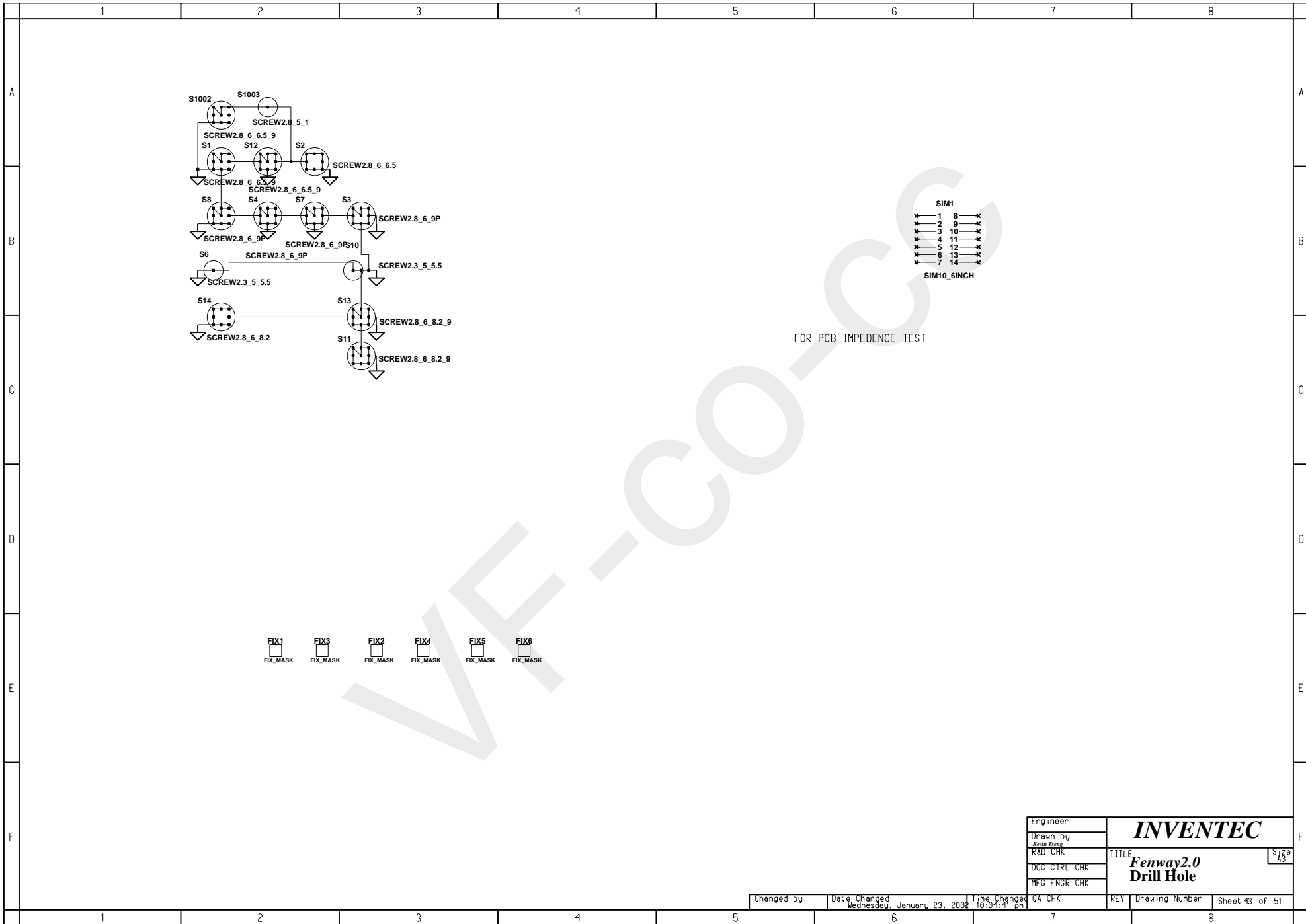
(ICH) (ICH)



(GMCH) (GMCH)

F

Eng ineer	<b>INVENTEC</b>		
Drawn by			
Reviz Toing			
R&D CHK			TITLE: <b>Fenway2.0</b>
DOC CTRL CHK			Size 33
MFG ENGR CHK	<b>DECOPULING CAPS</b>		



SIM1

* 1	8	*
* 2	9	*
* 3	10	*
* 4	11	*
* 5	12	*
* 6	13	*
* 7	14	*

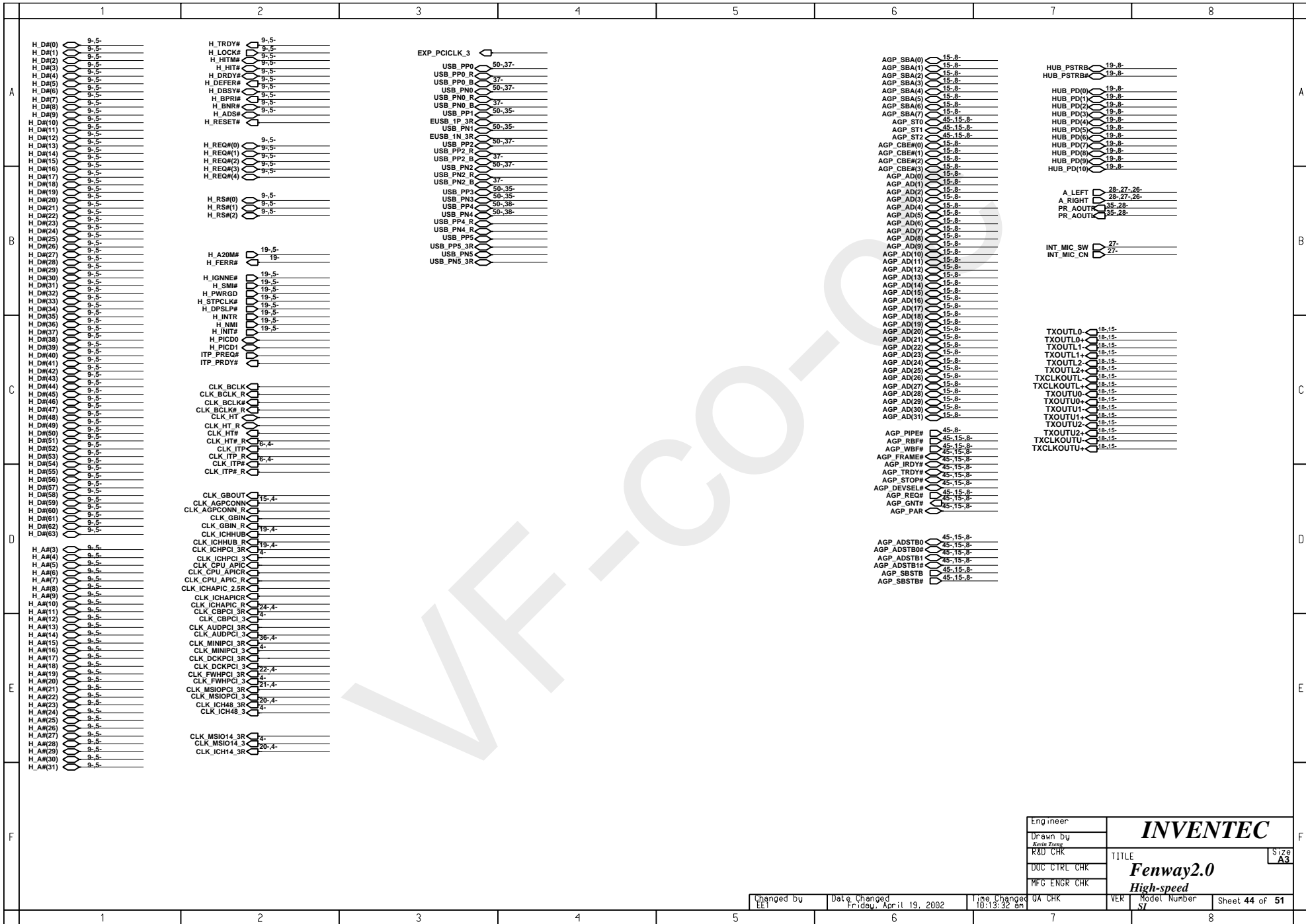
SIM10\_6INCH

FOR PCB IMPEDENCE TEST

- FIX1  
FIX\_MASK
- FIX3  
FIX\_MASK
- FIX2  
FIX\_MASK
- FIX4  
FIX\_MASK
- FIX5  
FIX\_MASK
- FIX6  
FIX\_MASK

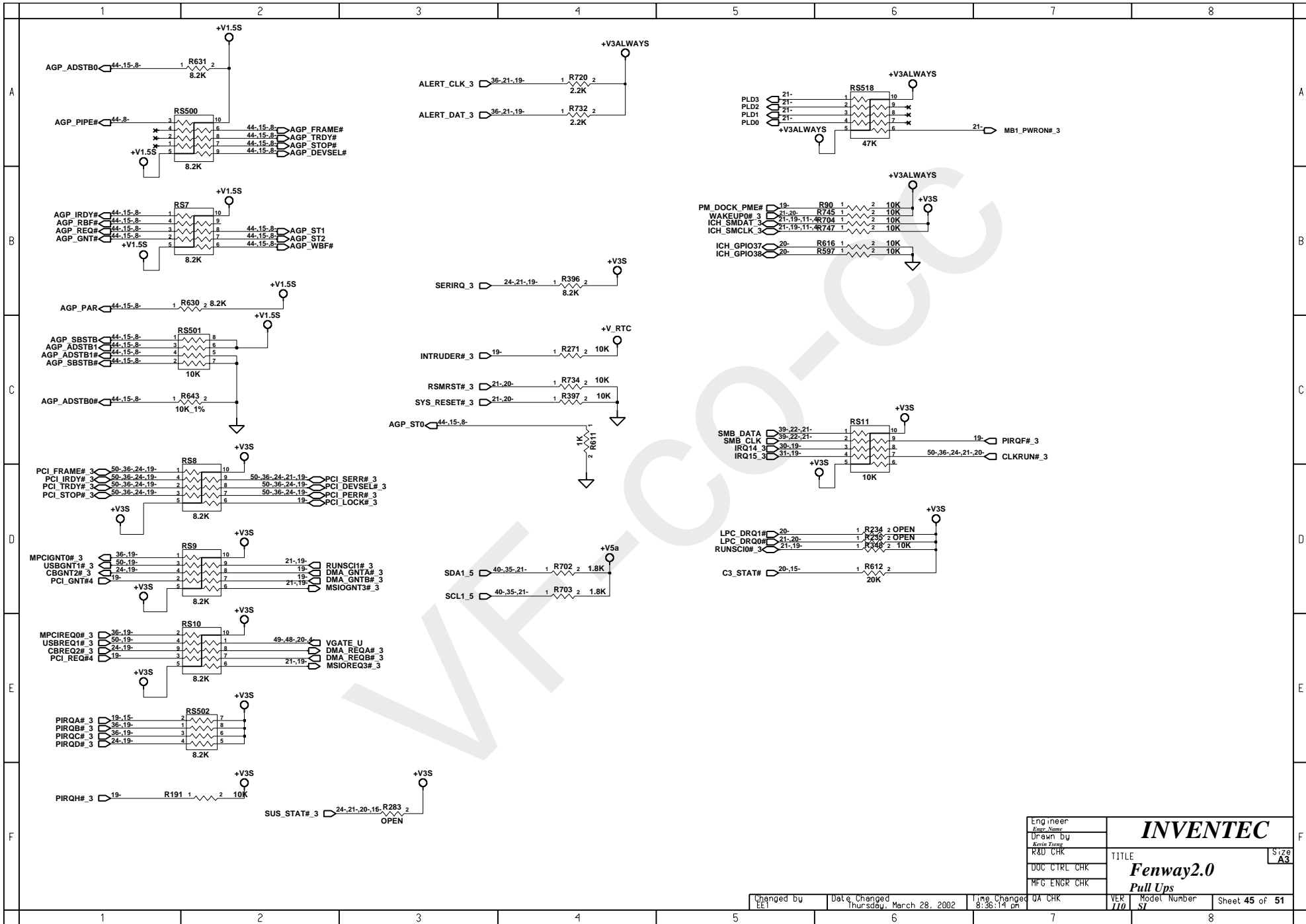
Engineer	<b>INVENTEC</b> TITLE: <b>Fenway2.0</b> <b>Drill Hole</b>		
Drawn by			
R3D CHK			
DOC CTRL CHK			
MFG ENGR CHK	REV	Drawing Number	Sheet 43 of 51

Changed by: [blank] Date Changed: Wednesday, January 23, 2008 Time Changed: 10:04:41 AM



Eng Ineer	<b>INVENTEC</b> TITLE <b>Fenway2.0</b> High-speed	Size <b>A3</b>
Drawn By		
Rev'd By		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK	Model Number	Sheet 44 of 51

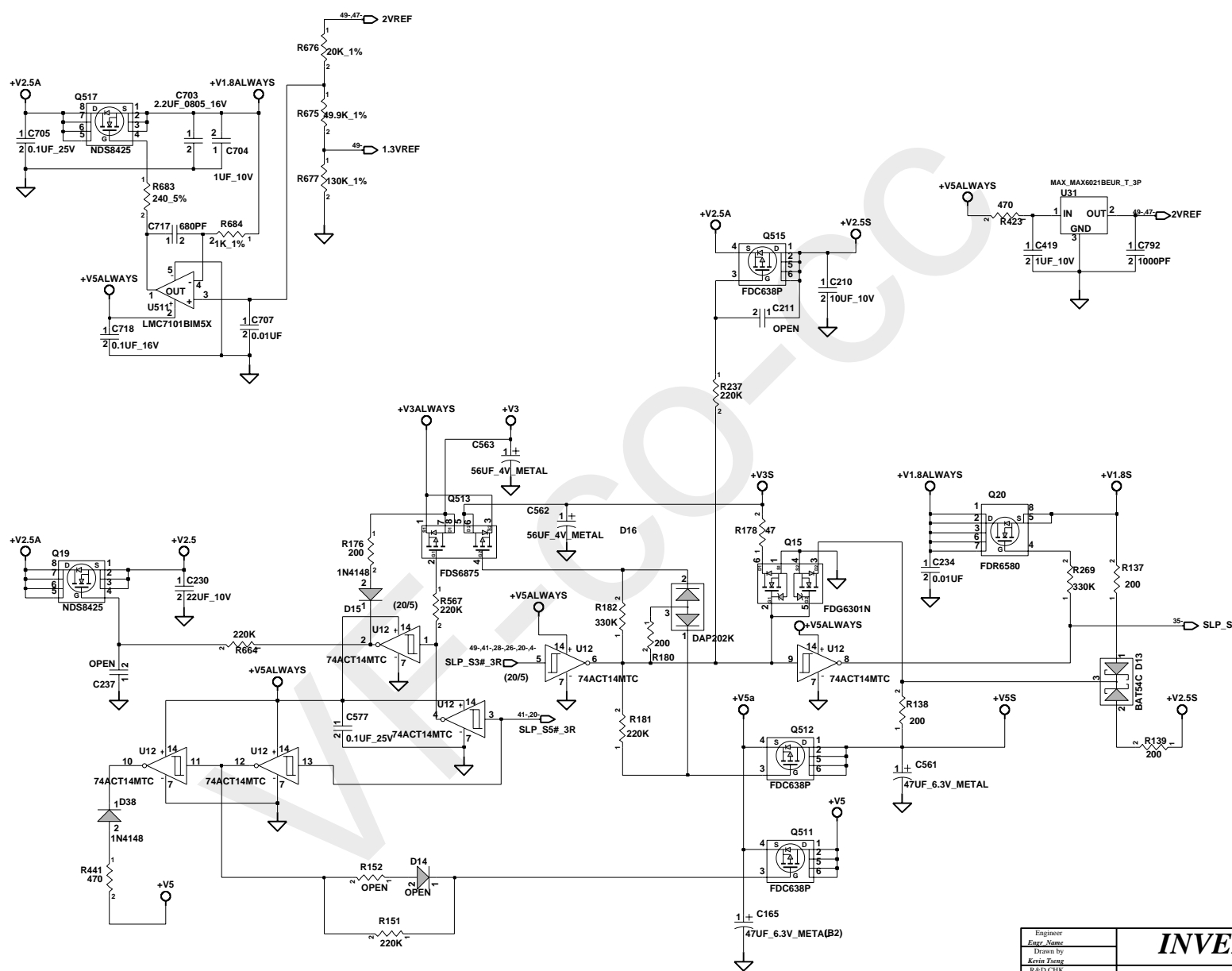
Changed by: EET  
 Date Changed: Friday, April 19, 2002  
 Time Changed: 10:13:32 am



Engineer	
Drawn by	
Rev'd	
Checked	
DOC CTRL	
MFG ENGR	
<b>INVENTEC</b>	
TITLE	S178 A3
<b>Fenway2.0</b>	
Pull Ups	

Changed by	Date Changed	Time Changed	QA CHK	VER	Model Number	Sheet
EE1	Thursday, March 28, 2002	8:36:14 pm		110	S1	45 of 51

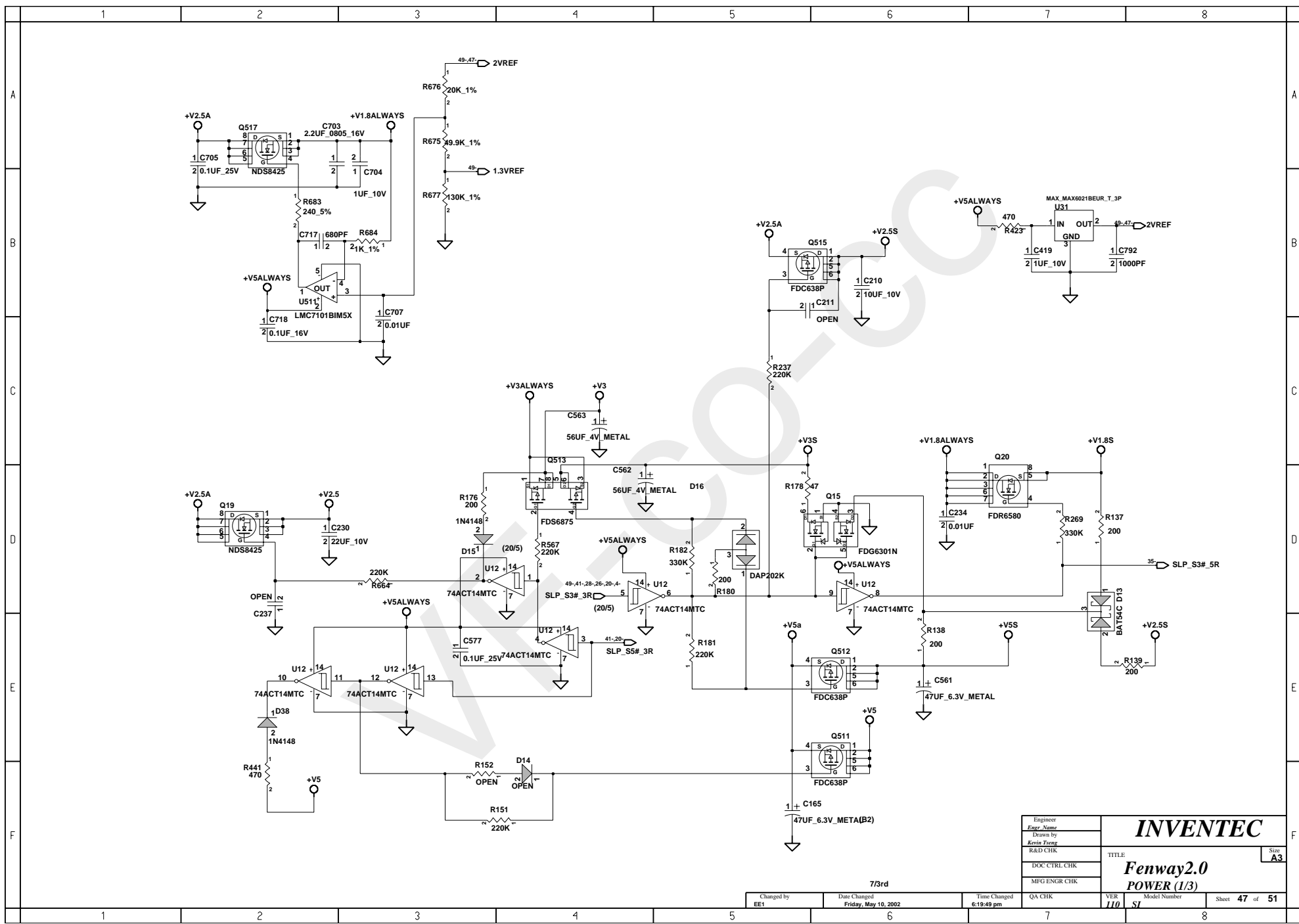




Engineer Engr Name		<b>INVENTEC</b>	Size <b>A3</b>
Drawn by Kevin Tseng			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			
TITLE	<b>Fenway2.0</b>		
	<b>POWER (1/3)</b>		
VER	Model Number	Sheet	51

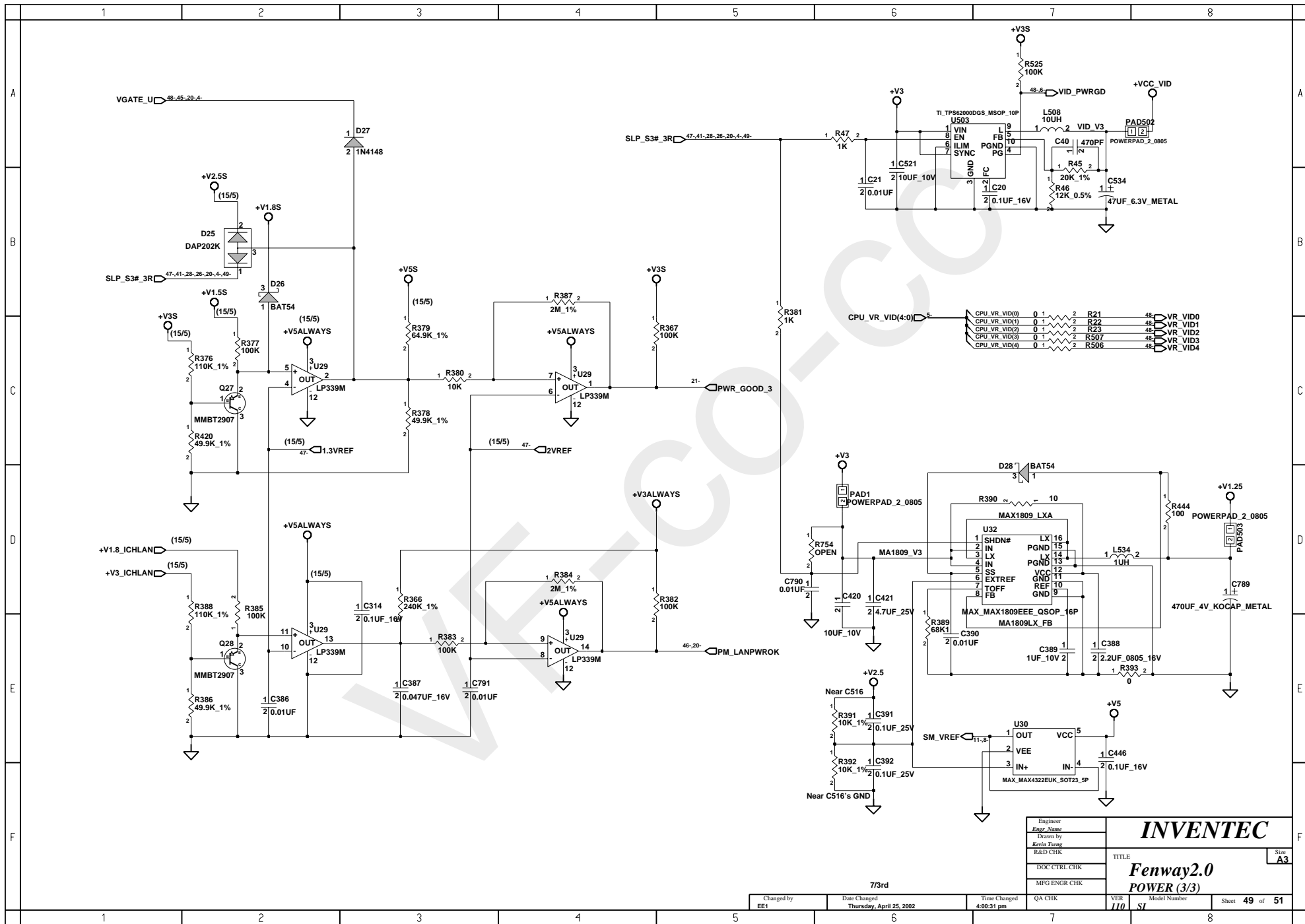
Changed by EE1	Date Changed Friday, May 10, 2002	Time Changed 6:19:49 pm	QA CHK
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7/3rd









Engineer <i>Engy Name</i>	<b>INVENTEC</b> TITLE: <b>Fenway2.0</b> <b>POWER (3/3)</b> Size: <b>A3</b>
Drawn by <i>Kevin Tseng</i>	
R&D CHK	
DOC CTRL CHK	
MFG ENGR CHK	

7/3rd	QA CHK	VER <b>110</b>	Model Number <b>SI</b>	Sheet <b>49</b> of <b>51</b>
Changed by <b>EE1</b>	Date Changed <b>Thursday, April 25, 2002</b>	Time Changed <b>4:00:31 pm</b>		



